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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Confirmation No. 7022

Mitsuaki OSHIMA et al.

Docket No. 2000\_1328

Serial No. 09/672,946

Group Art Unit 2634

Filed September 29, 2000

Examiner A. Le

**COMMUNICATION SYSTEM** 

THE COMMISSIONER IS AUTHORIZED TO CHARGE ANY DEFICIENCY IN THE FEES FOR THIS PAPER TO DEPOSIT ACCOUNT NO. 23-0975

## **SUBMISSION OF FORMAL DRAWINGS**

Assistant Commissioner for Patents, Washington, D.C.

Sir:

As required by the Examiner in the Office Action mailed September 16, 2002, 178 sheets of formal (8½ x 11) drawings (Figs. 1-179) are submitted herewith.

Respectfully submitted,

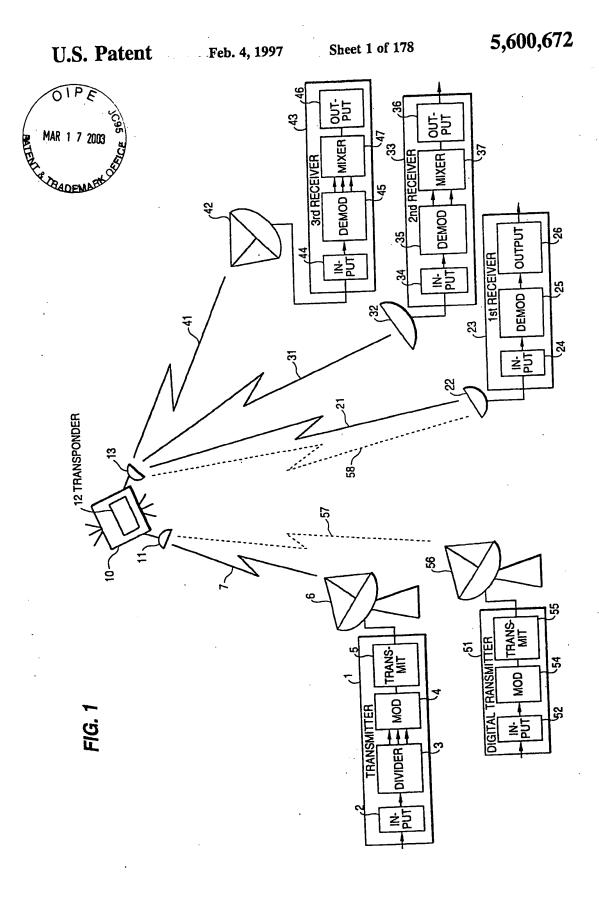
Mitsuaki OSHIMA et al.

R. Fälipek ration No. 41,471

Attorney for Patentees

JRF/fs

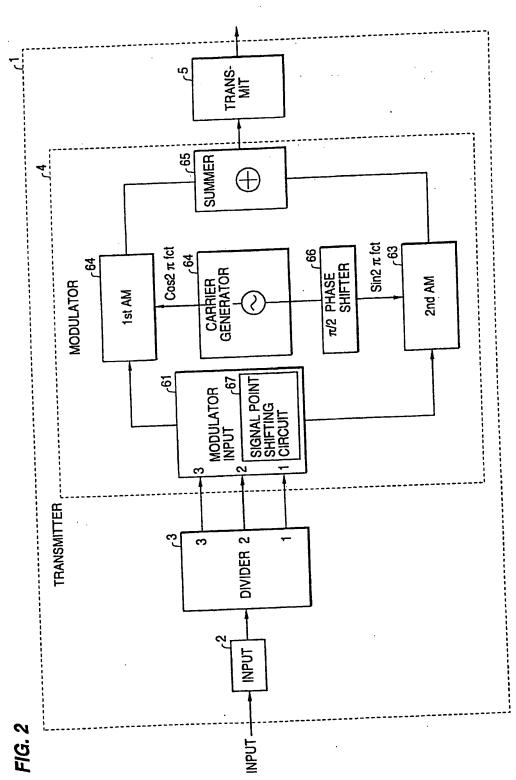
Washington, D.C. 20006-1021 Telephone (202) 721-8200 Facsimile (202) 721-8250 March 17, 2003



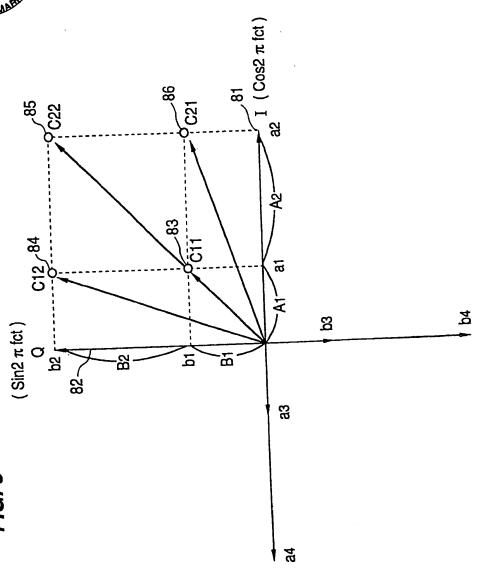
U.S. Patent

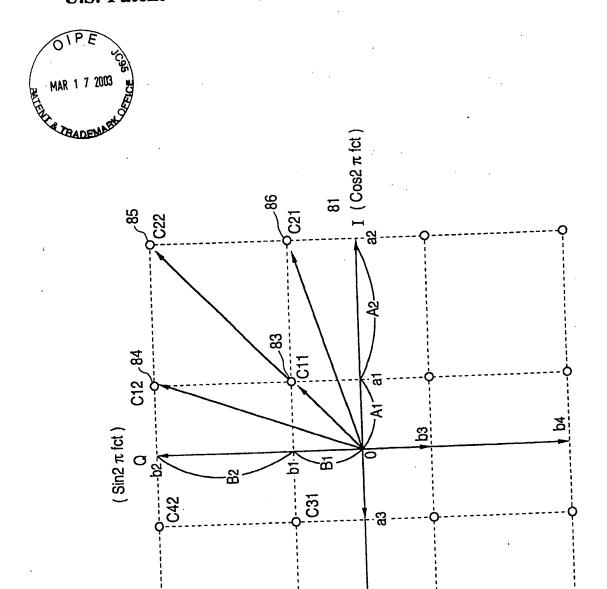
Feb. 4, 1997

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<sup>-</sup>1G. <sup>1</sup>

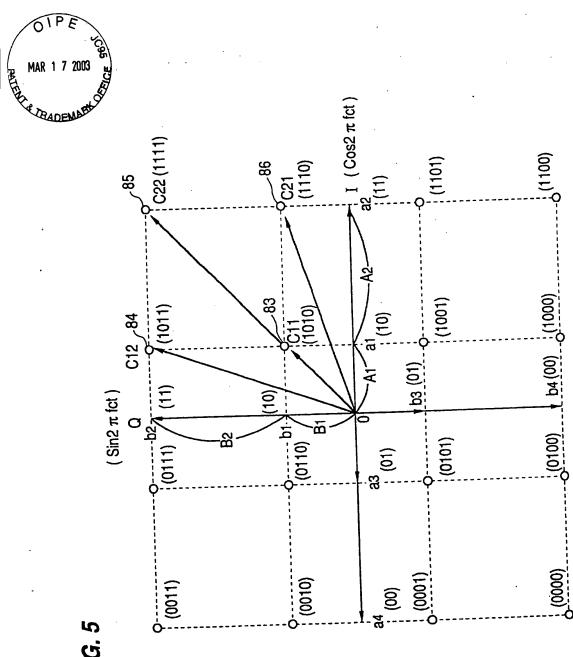


FIG.



FIG. 6

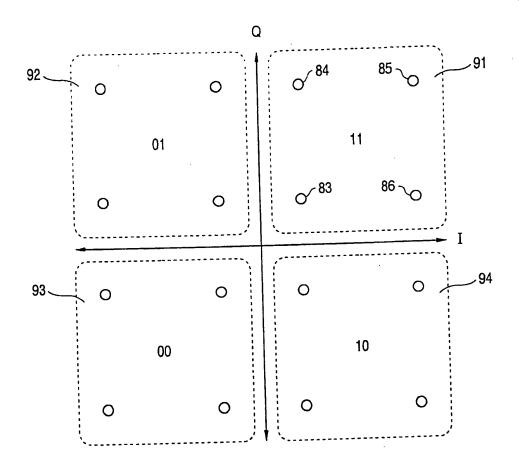


FIG. 7

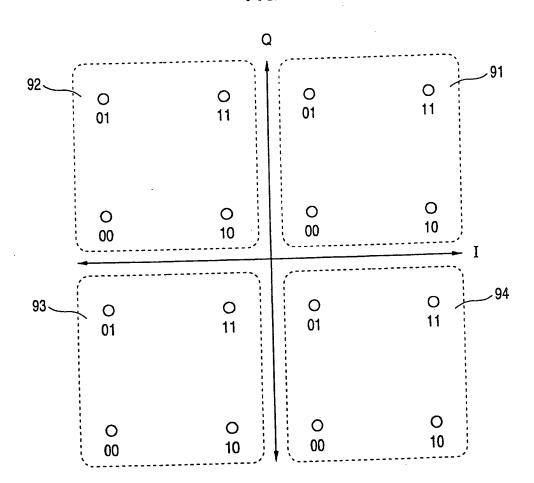
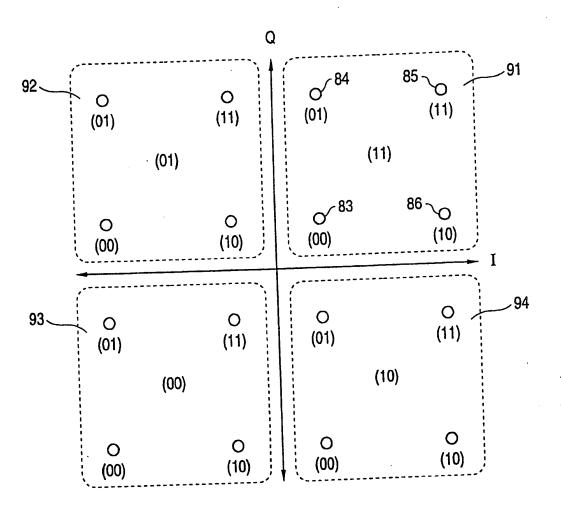
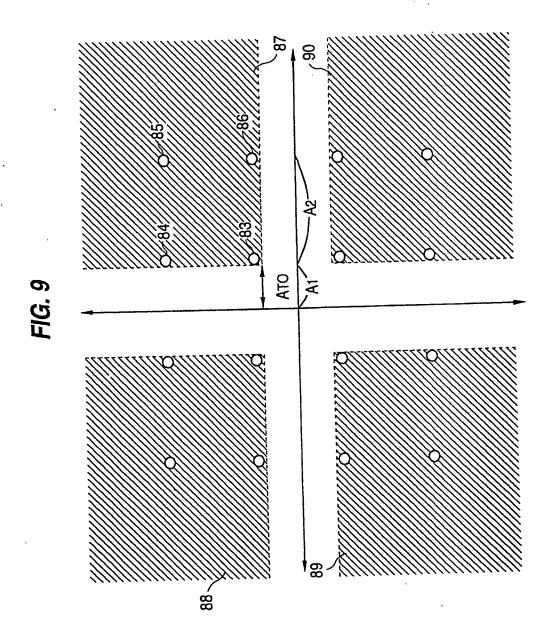
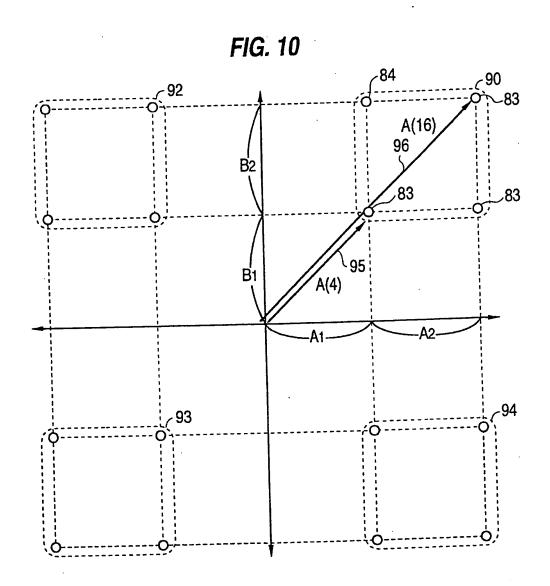


FIG. 8







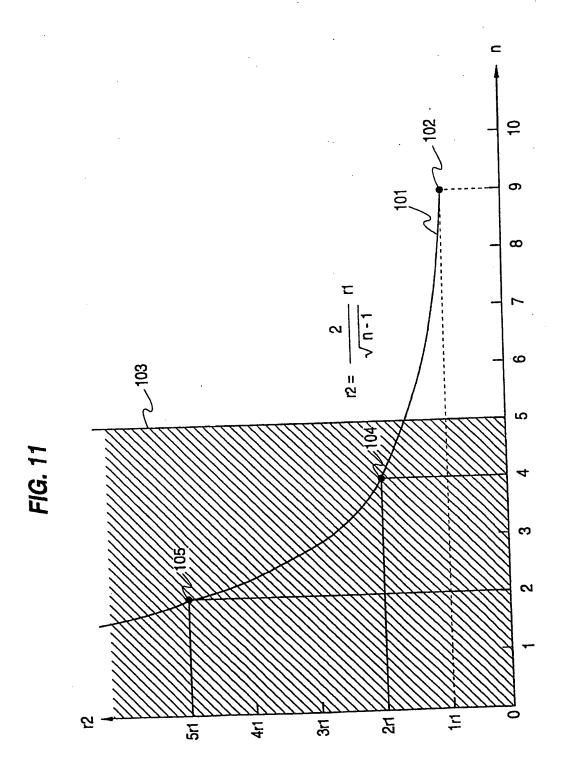
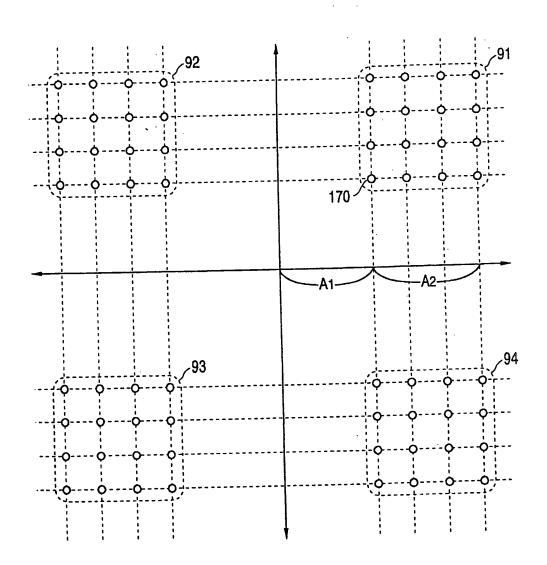


FIG. 12



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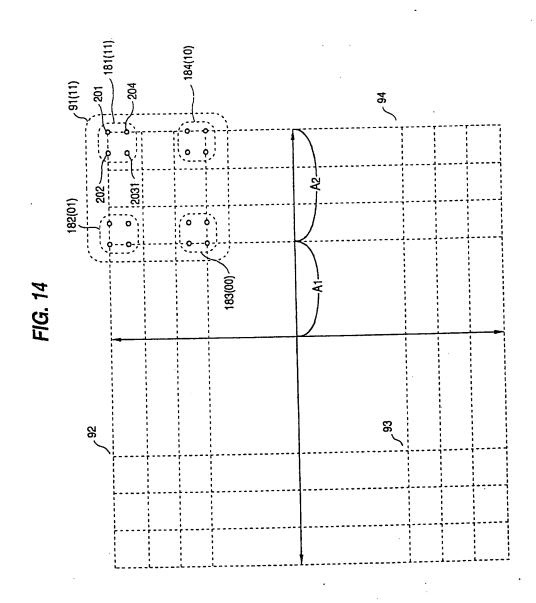
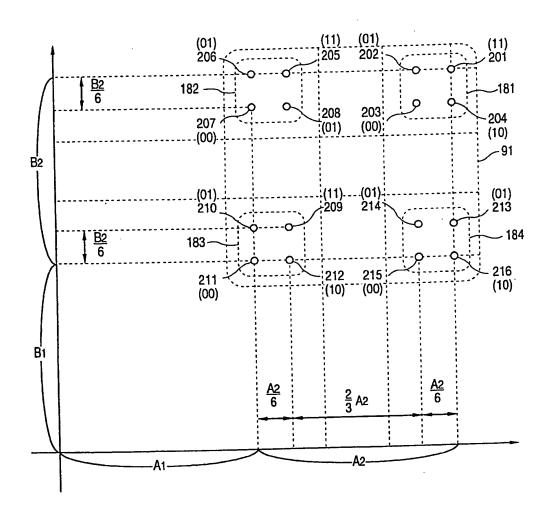
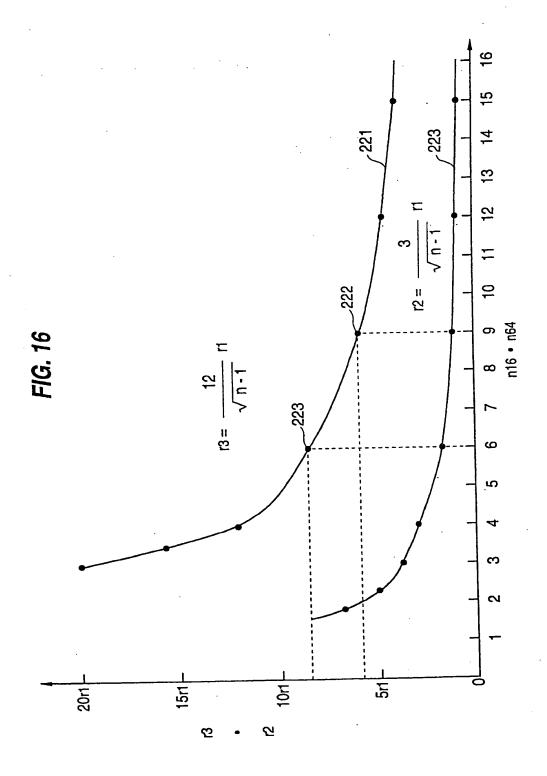


FIG. 15





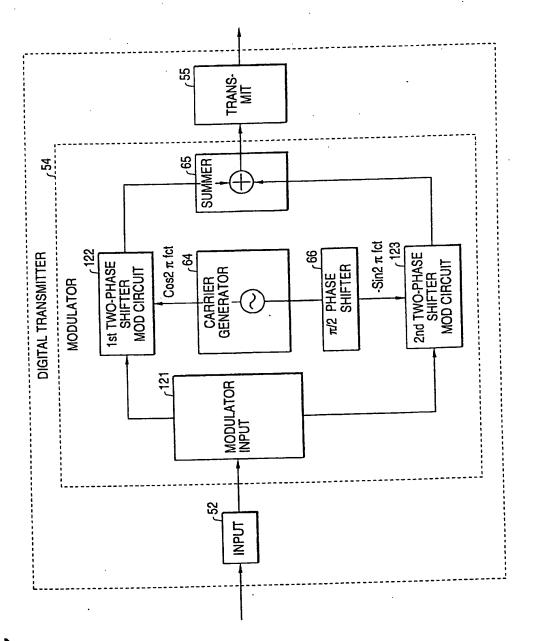
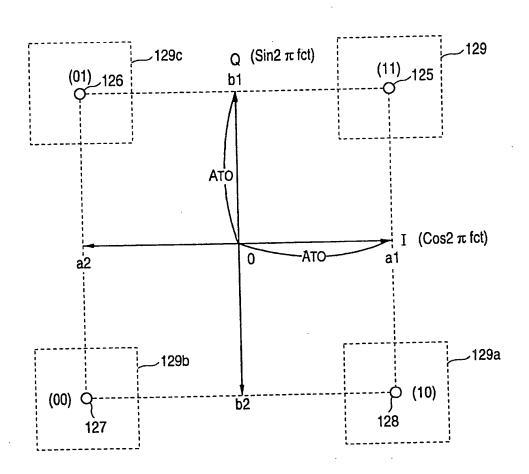
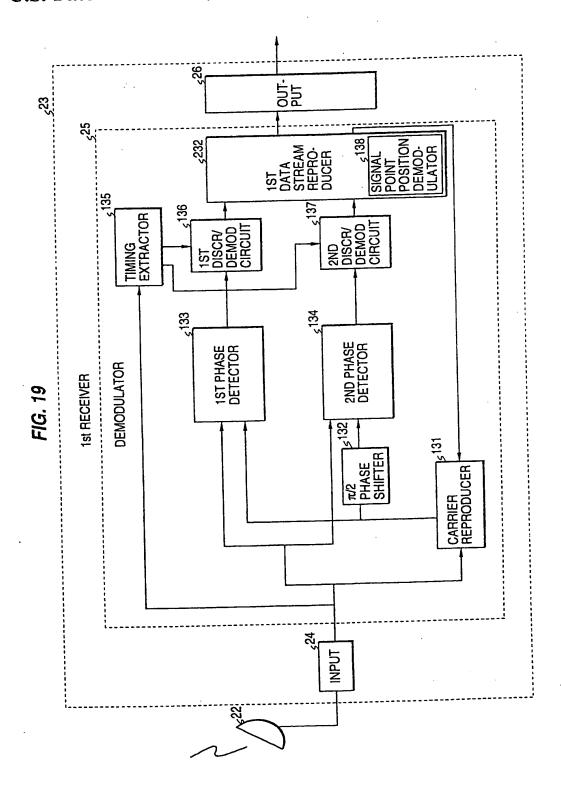
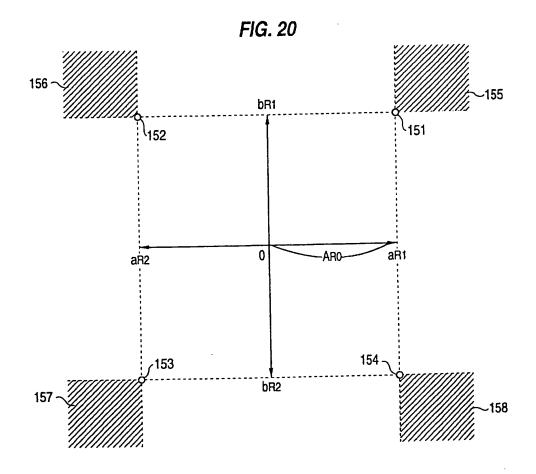


FIG. 17

FIG. 18







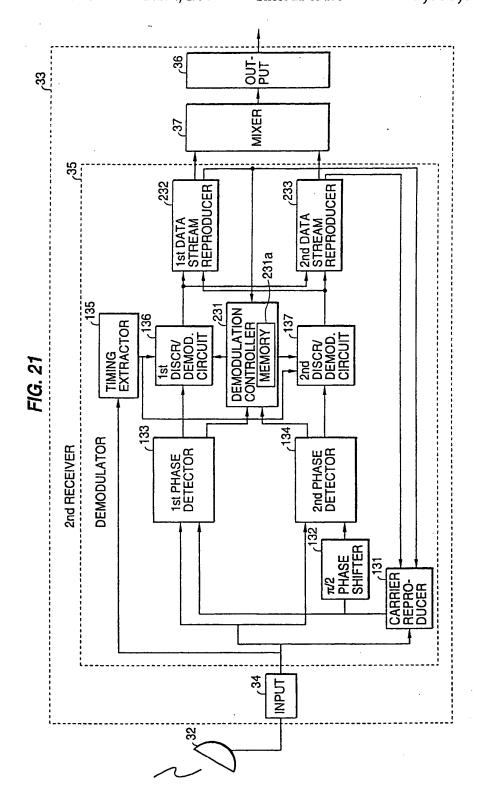
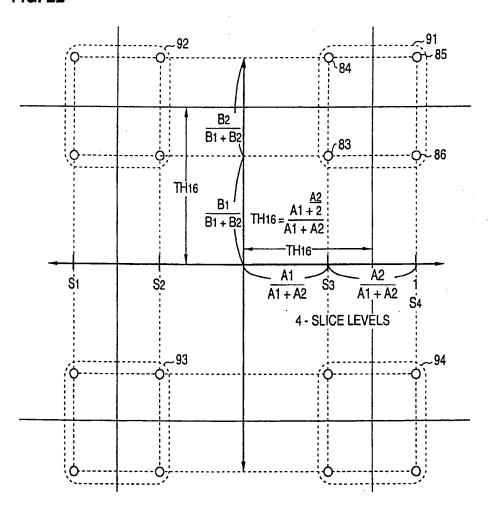


FIG. 22



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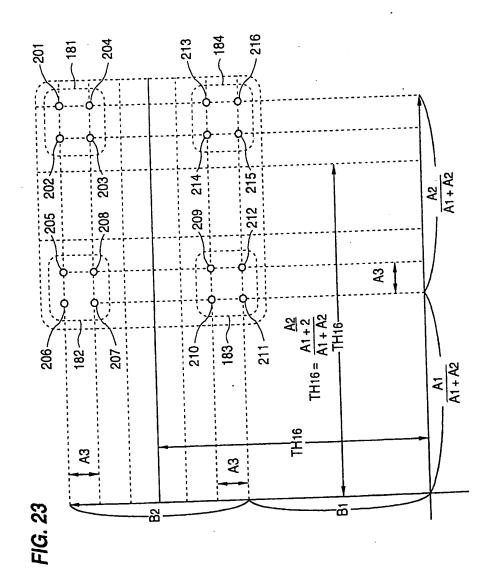


FIG. 24

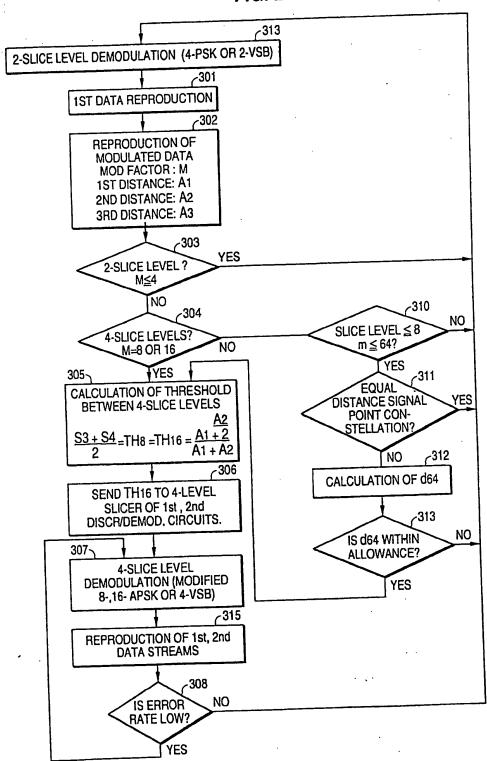


FIG. 25( a )

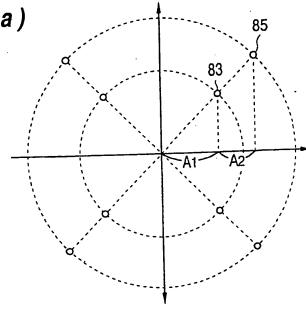
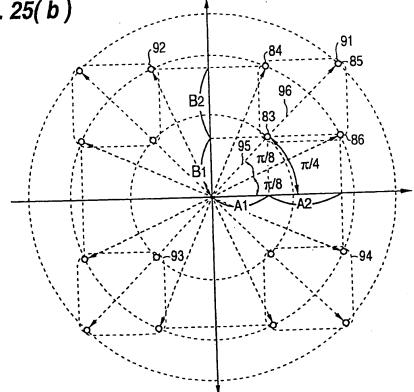
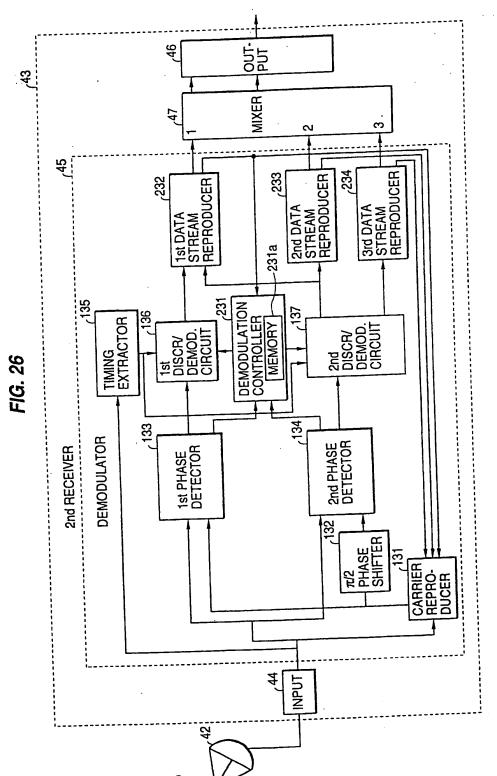
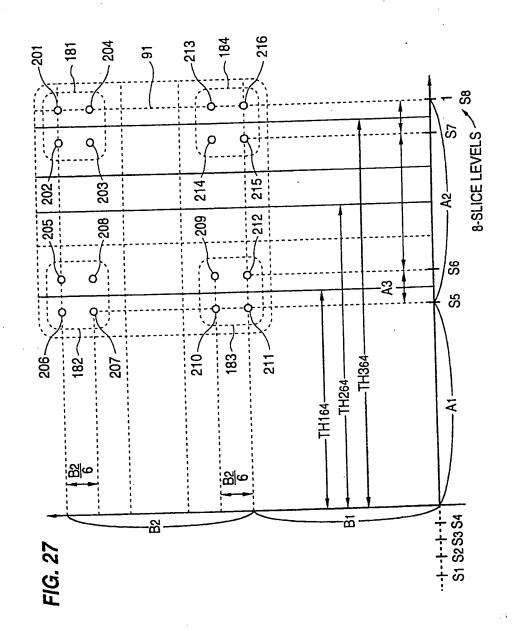
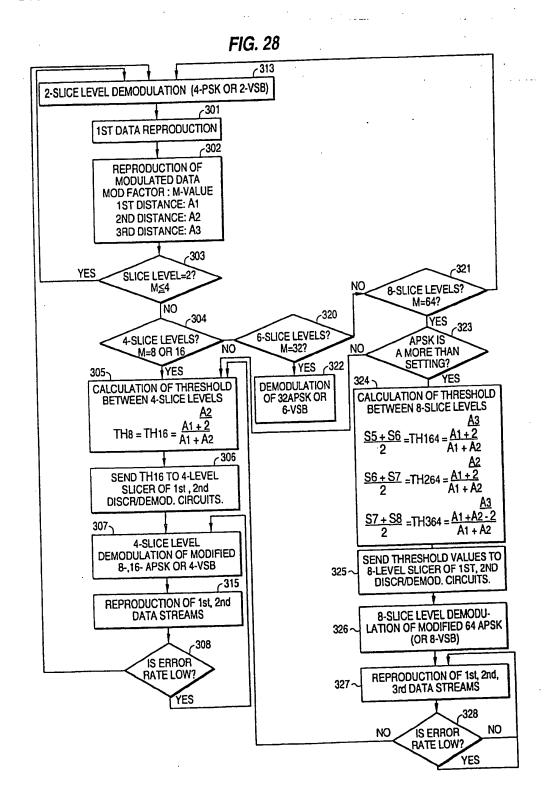


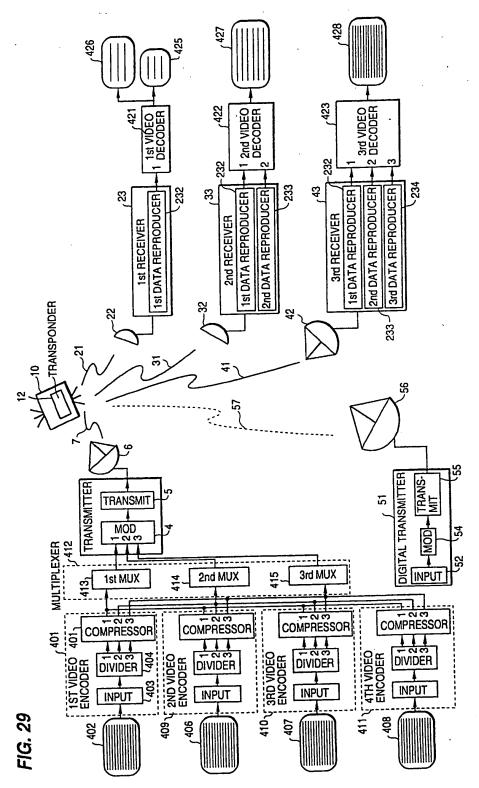
FIG. 25(b)

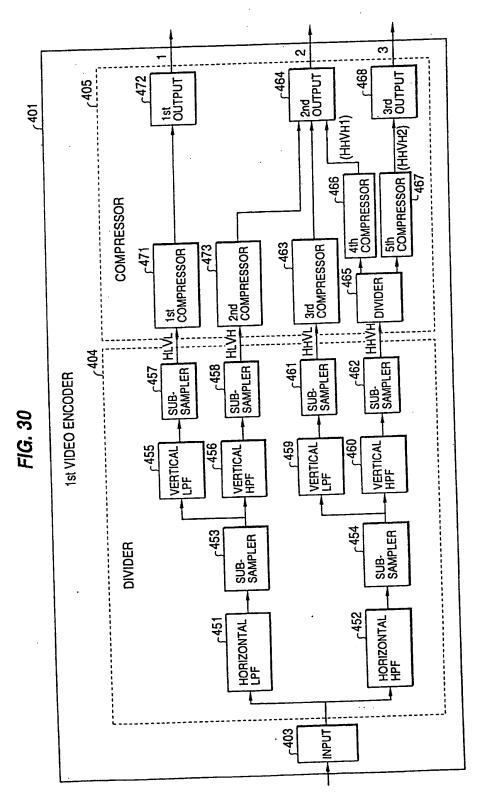


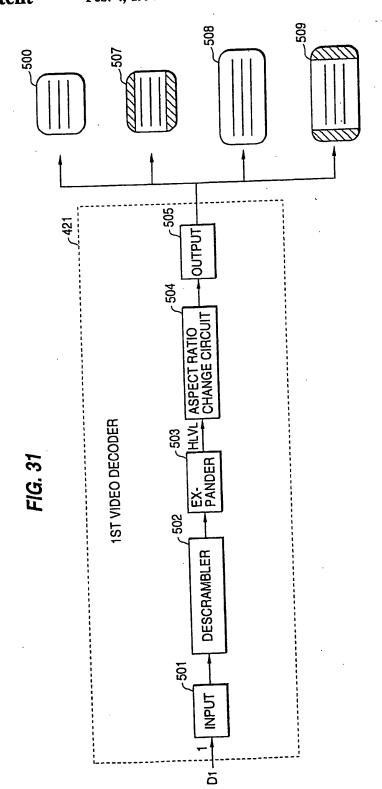


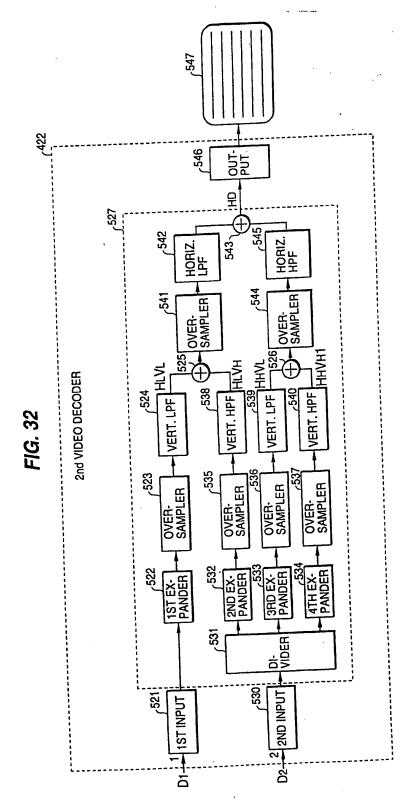


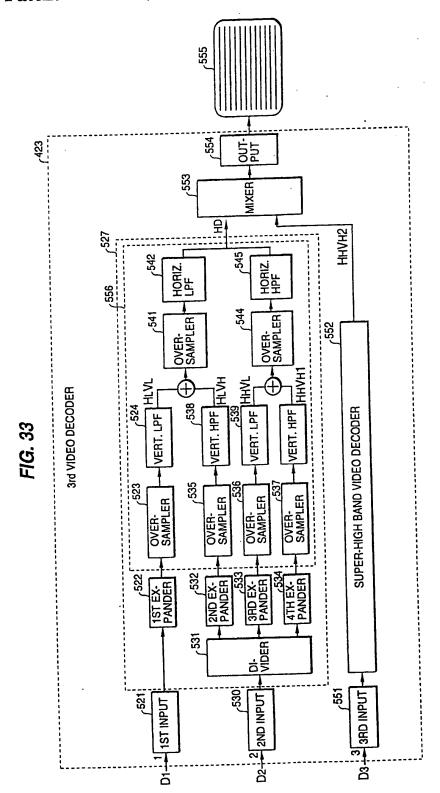


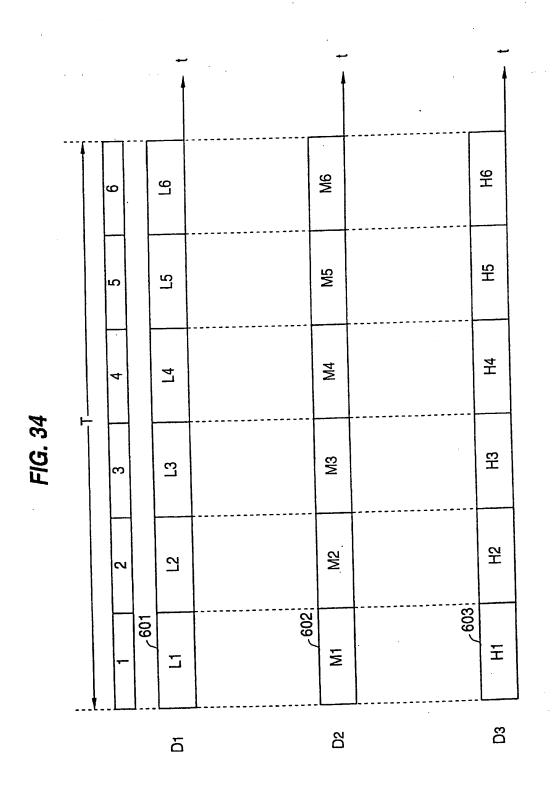




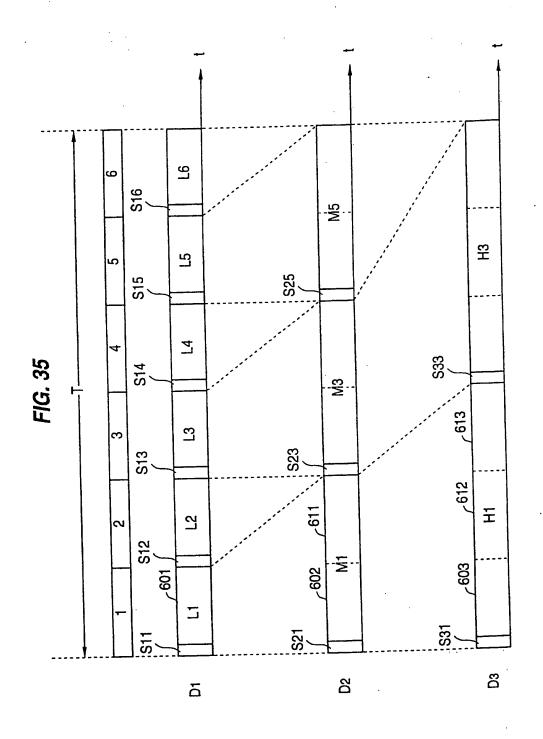


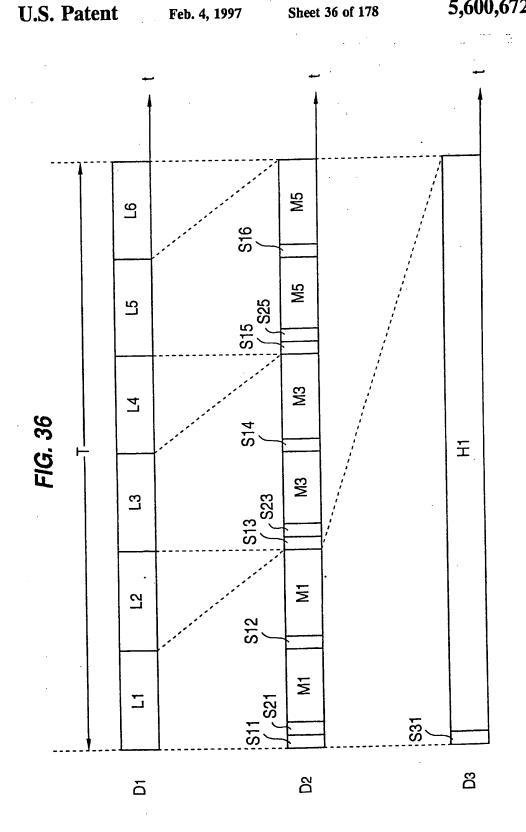


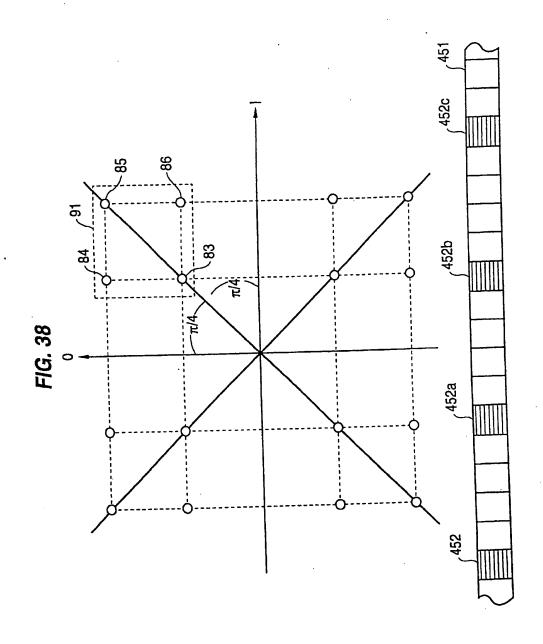


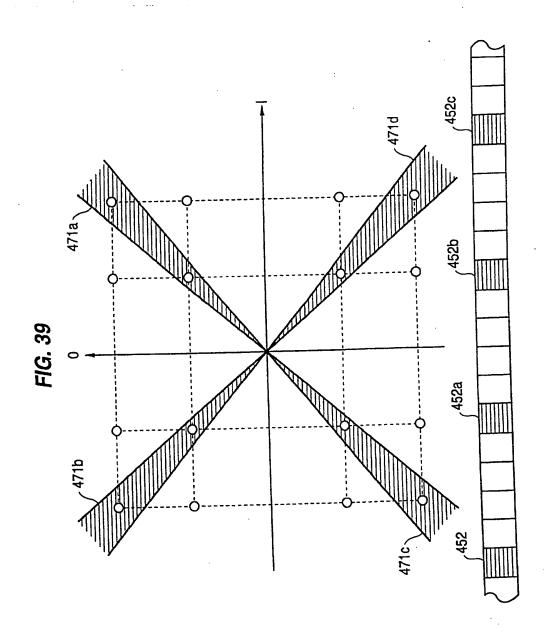


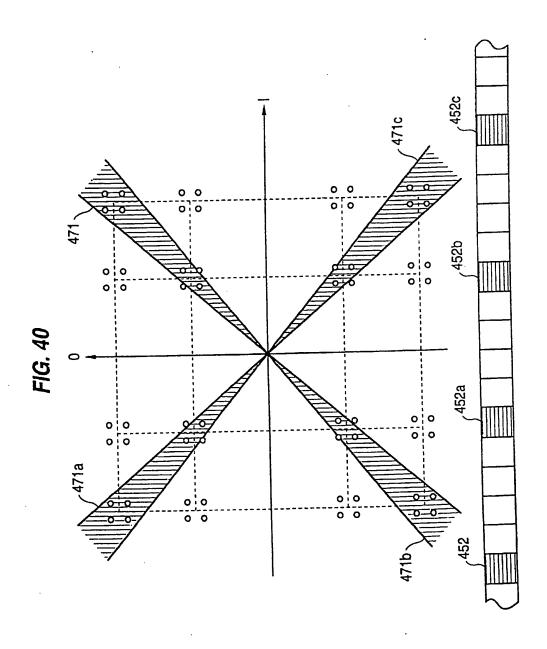
Feb. 4, 1997

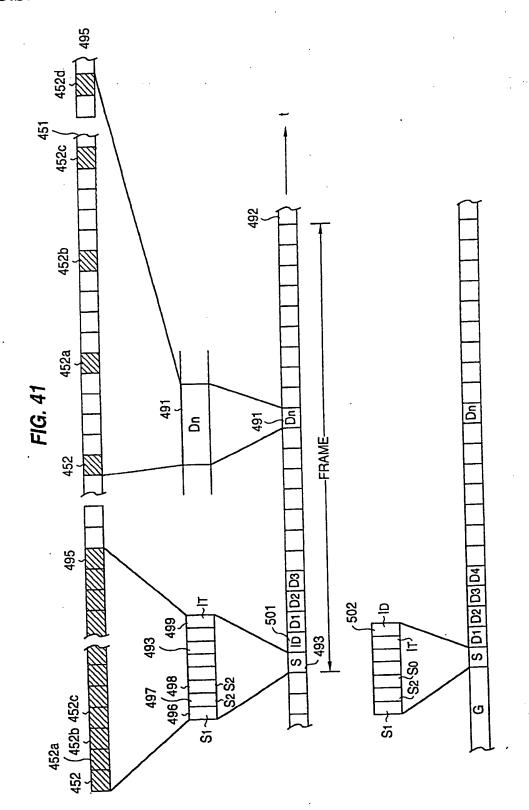




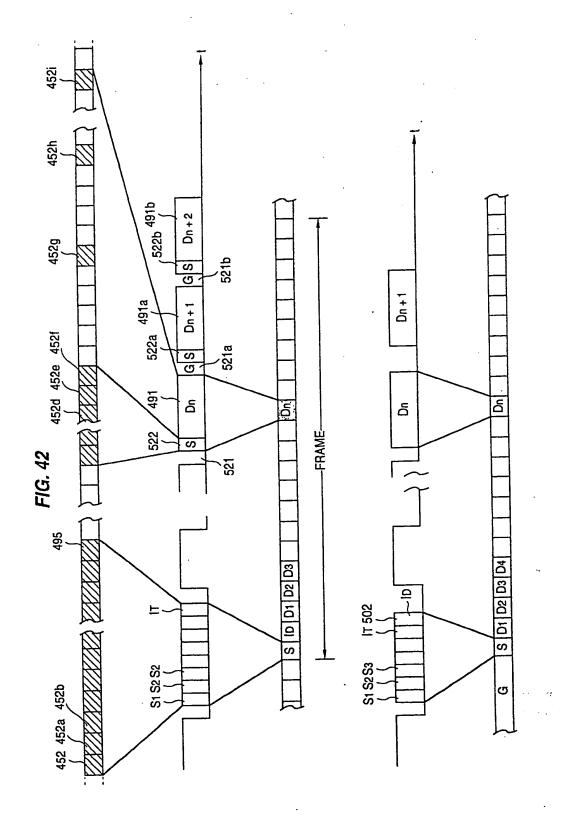


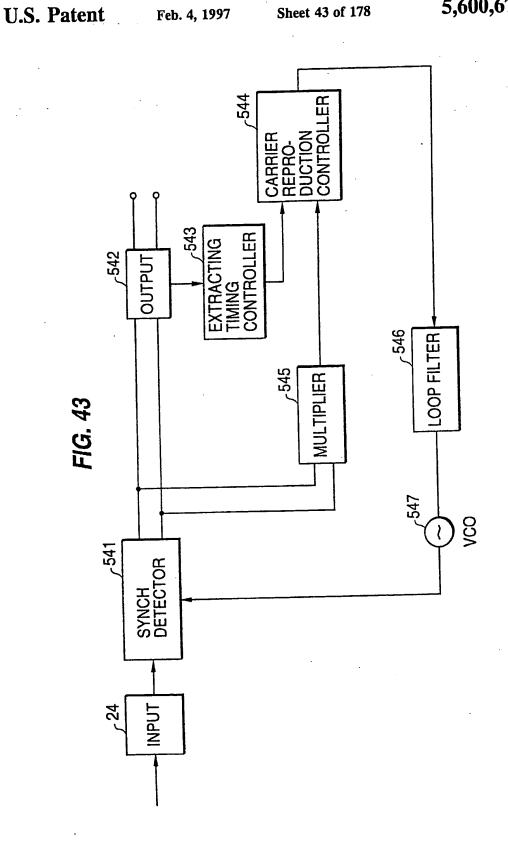


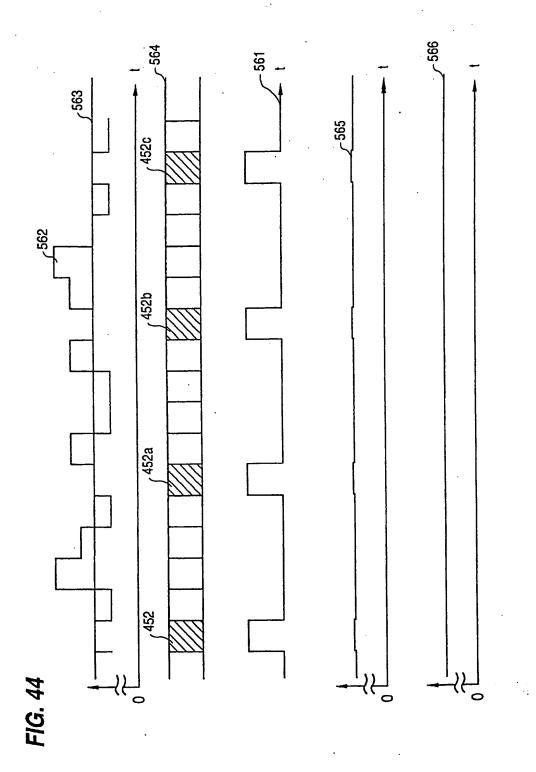


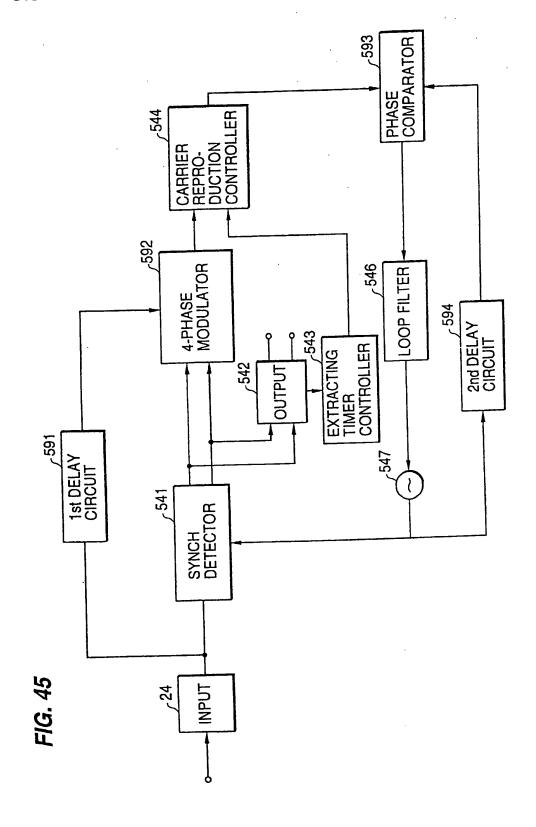


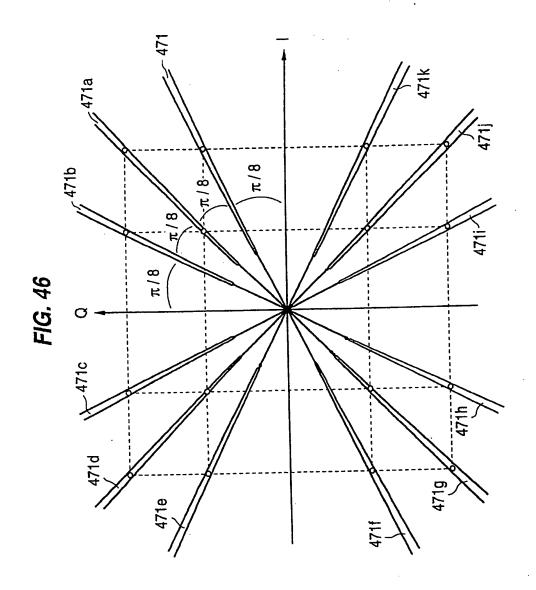
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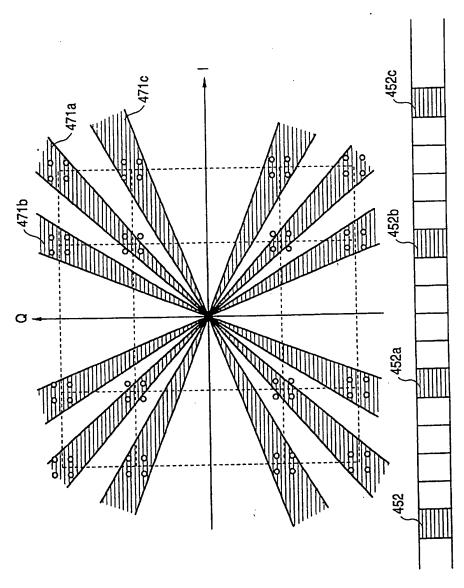




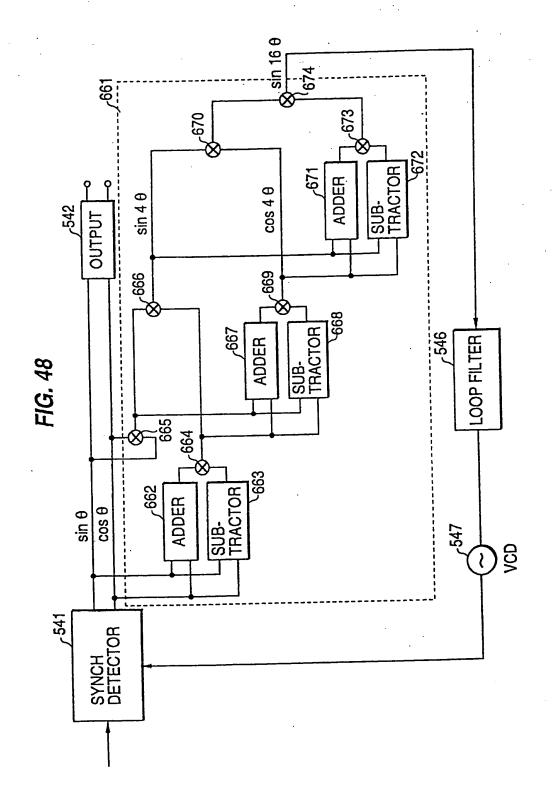






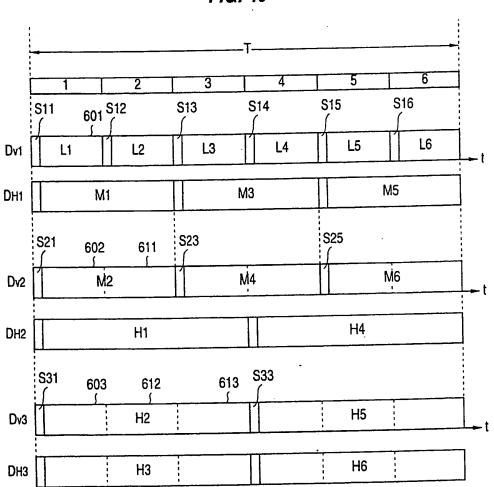


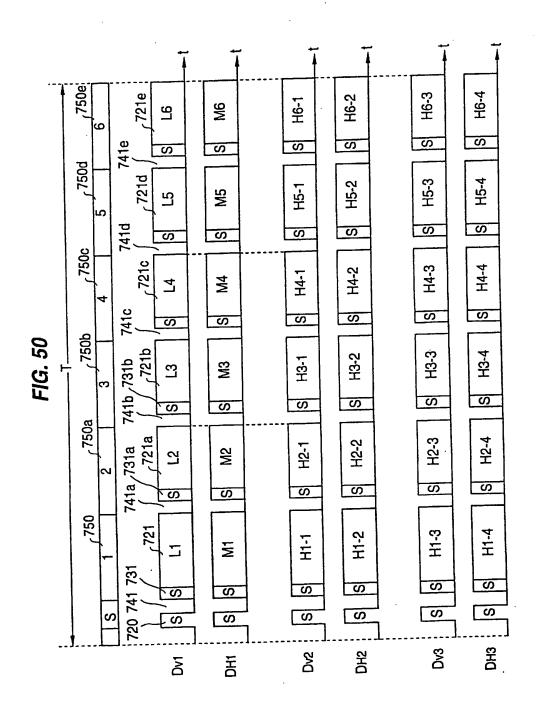
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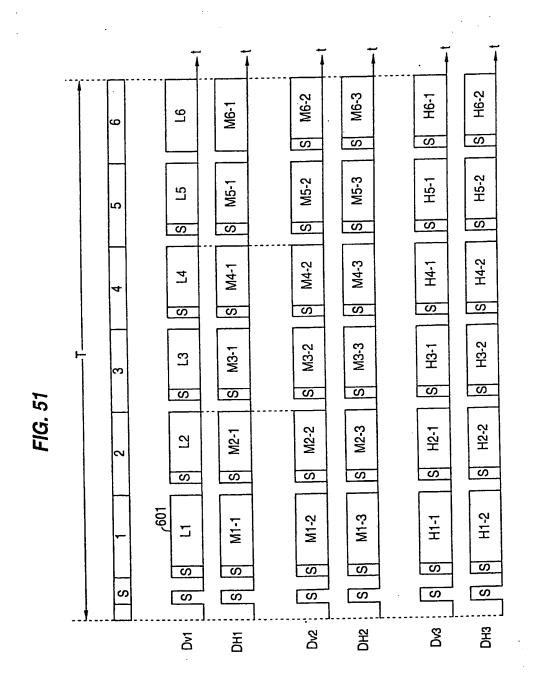


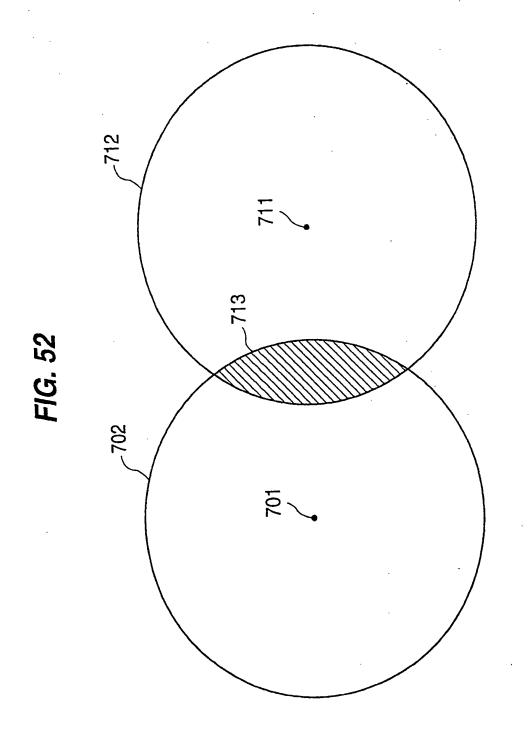
U.S. Patent

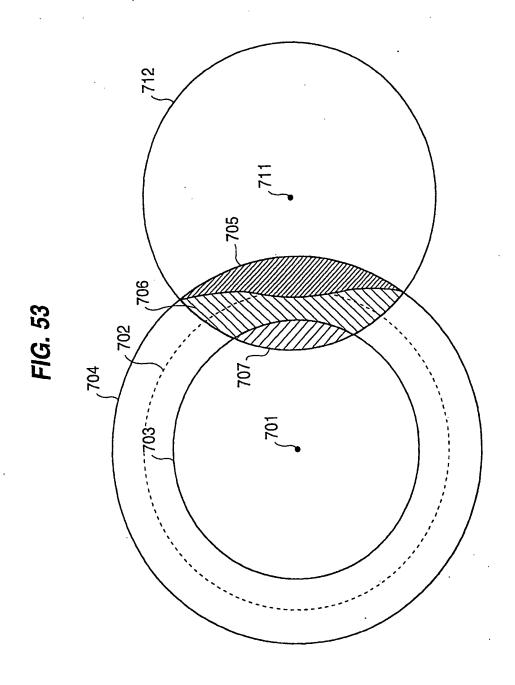
FIG. 49

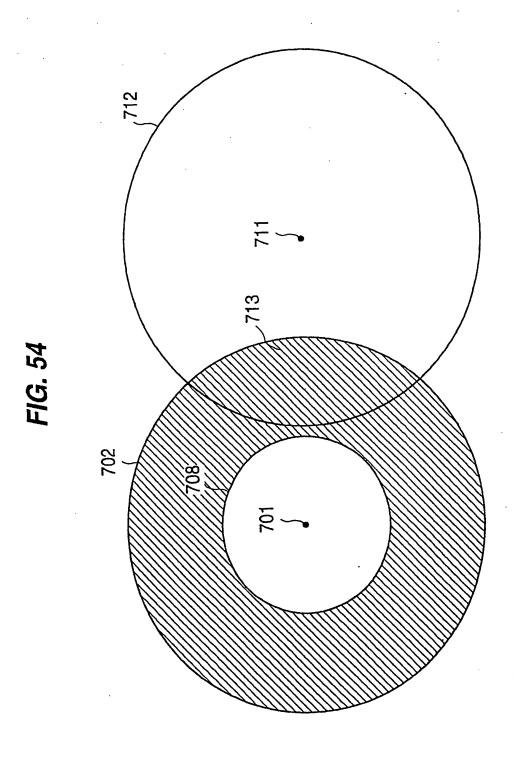












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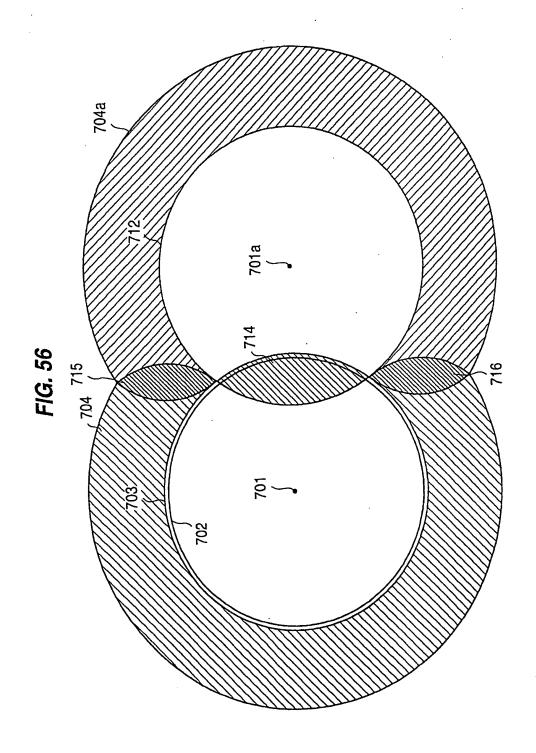


FIG. 57

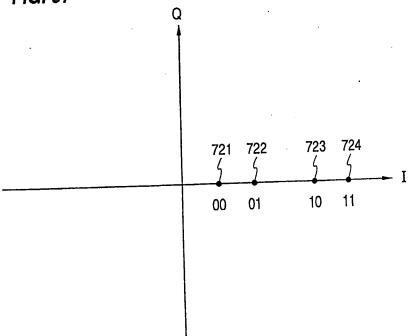
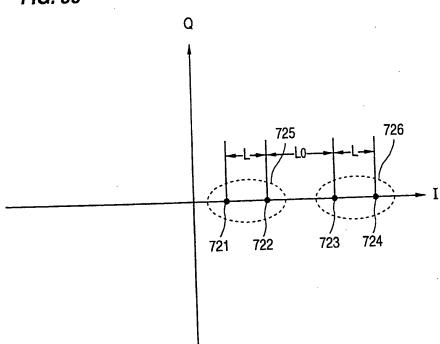
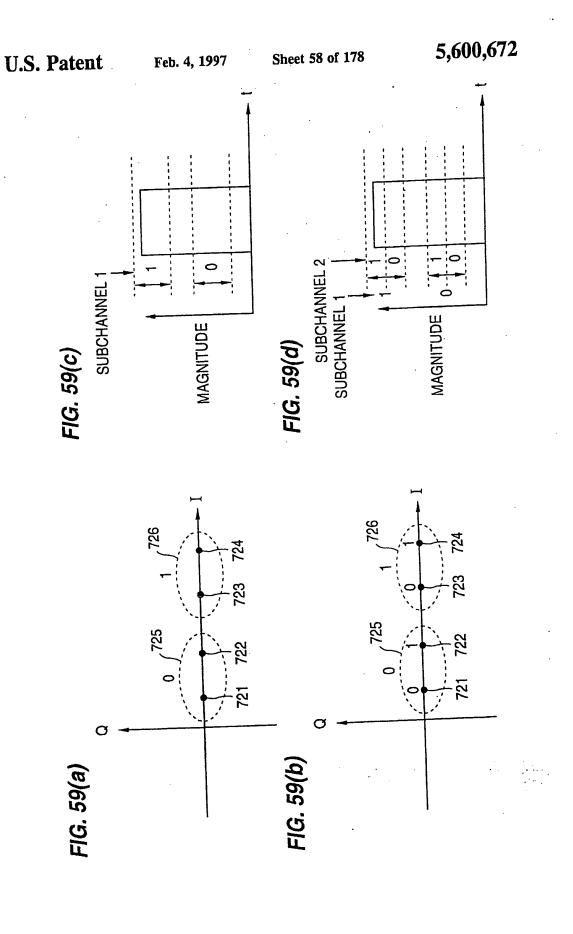
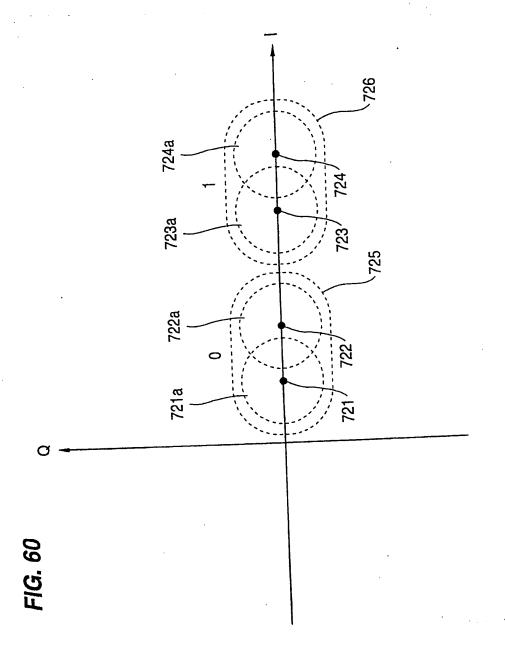


FIG. 58

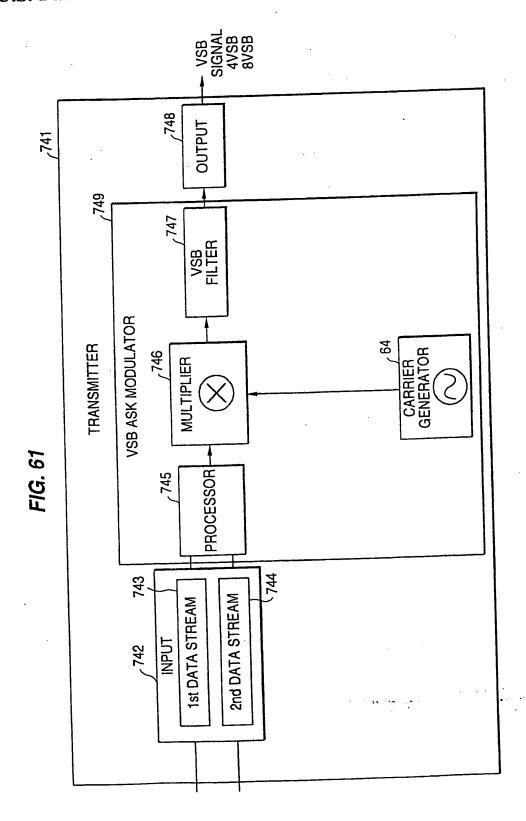


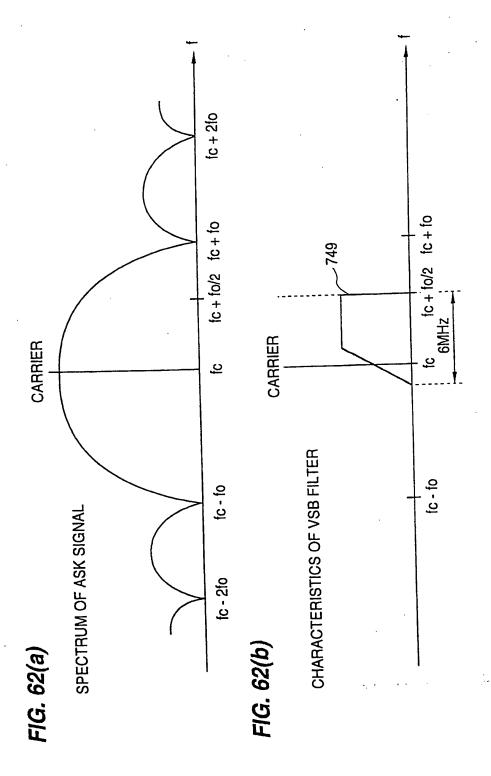


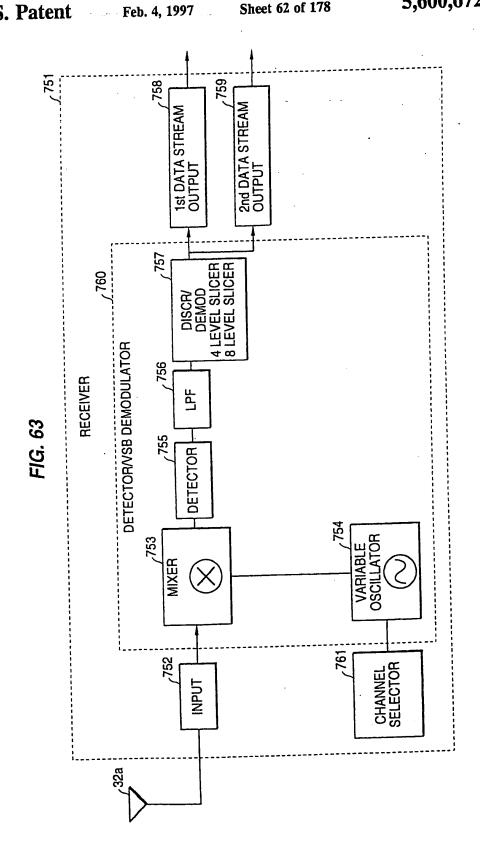
Feb. 4, 1997

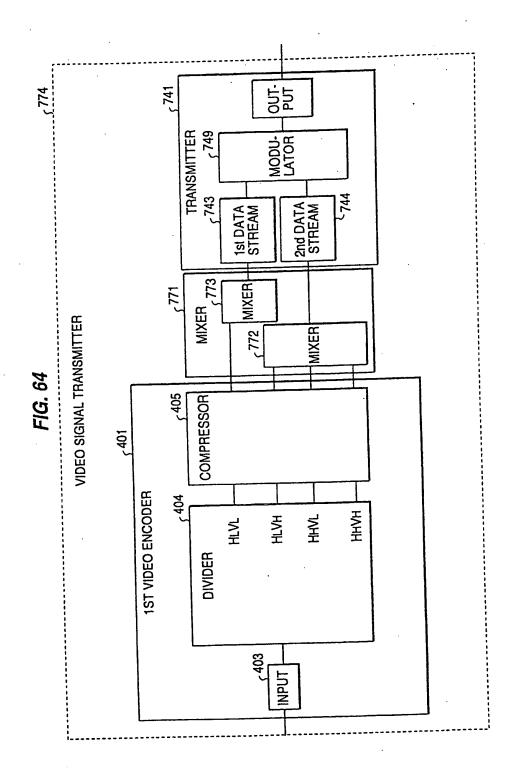


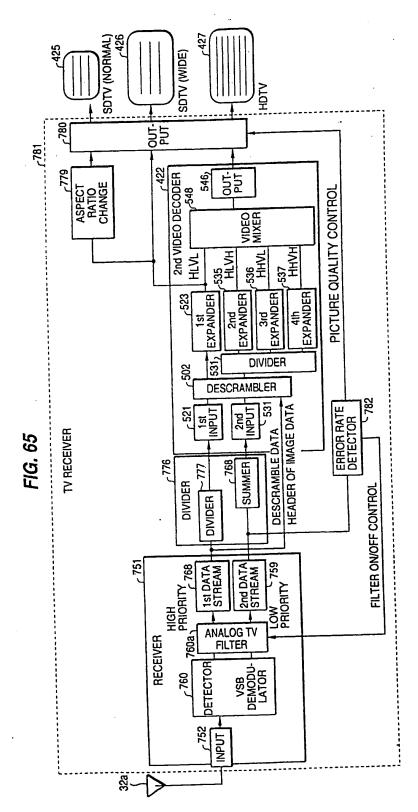
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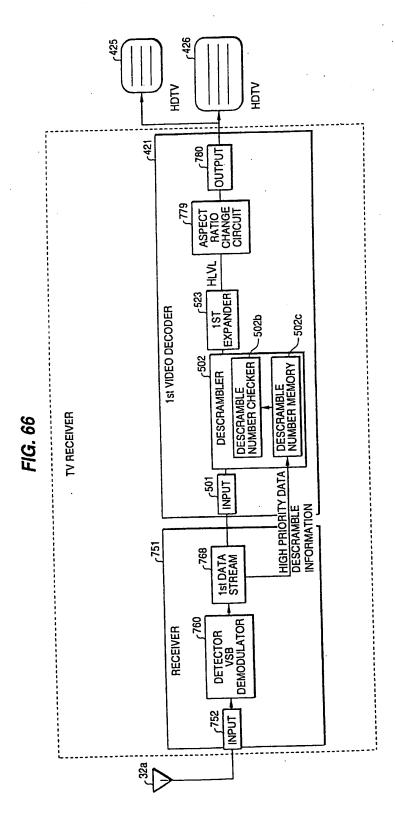


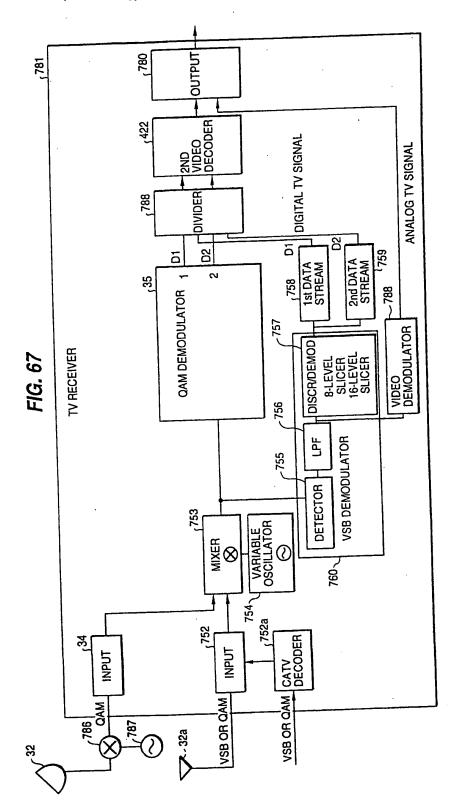




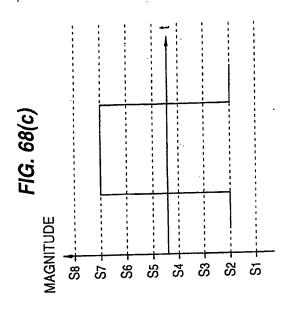


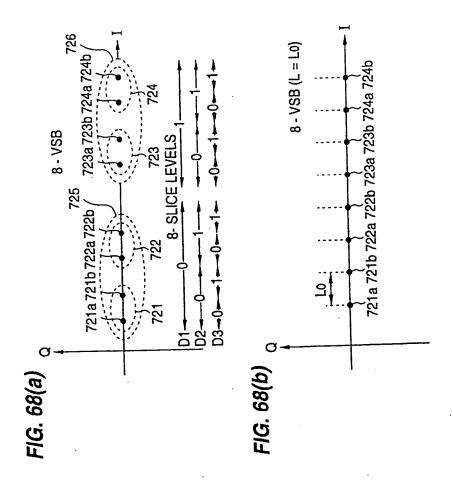


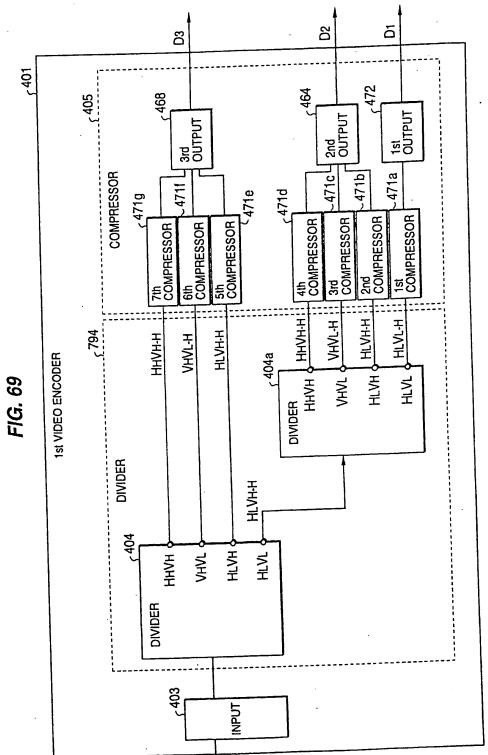


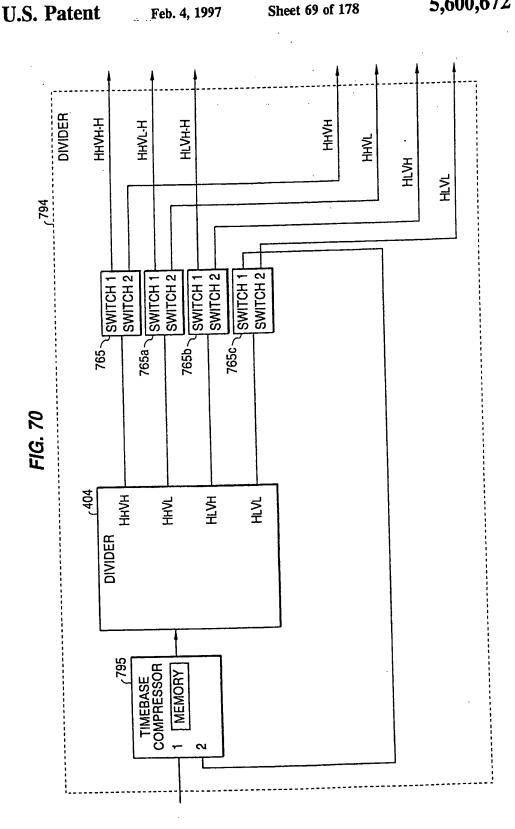


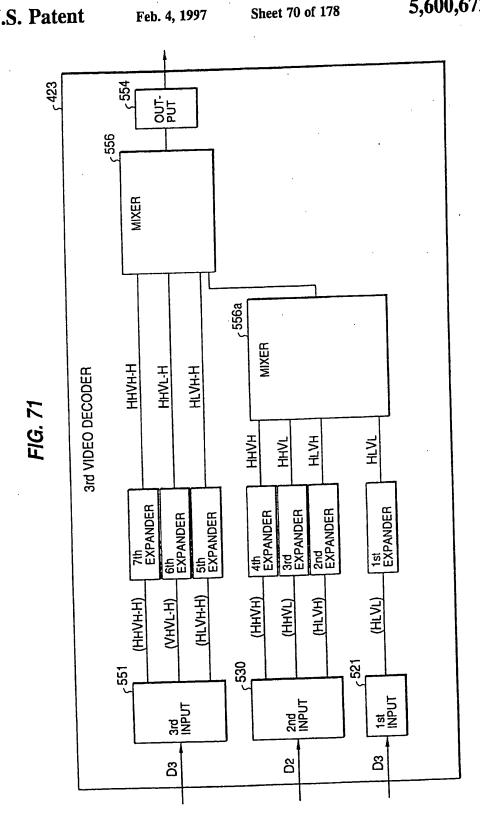
Feb. 4, 1997











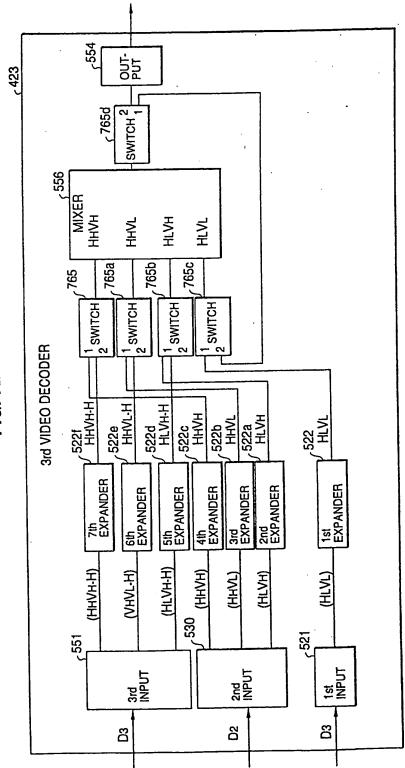
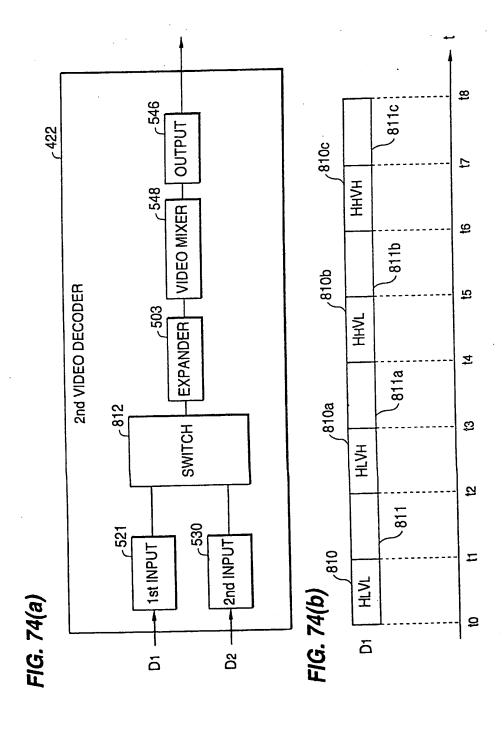
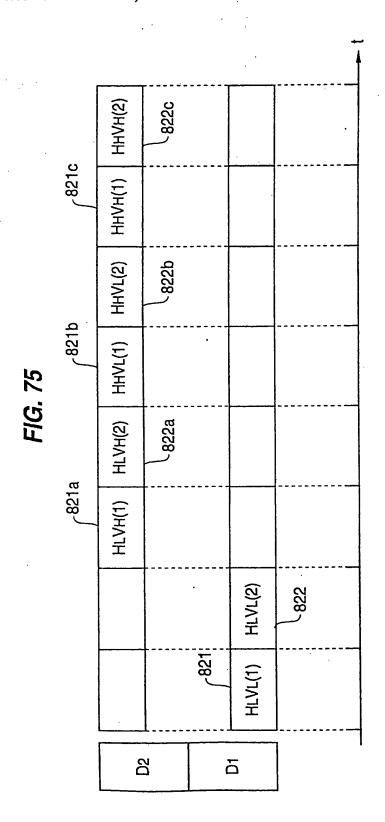


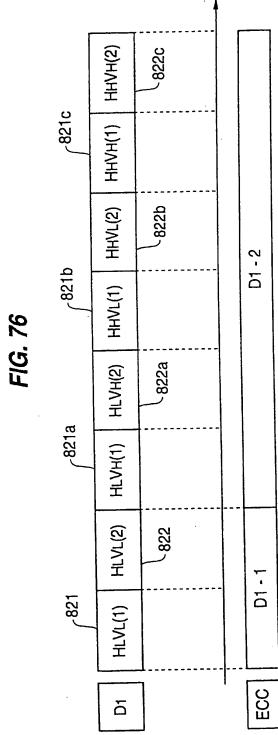
FIG. 72

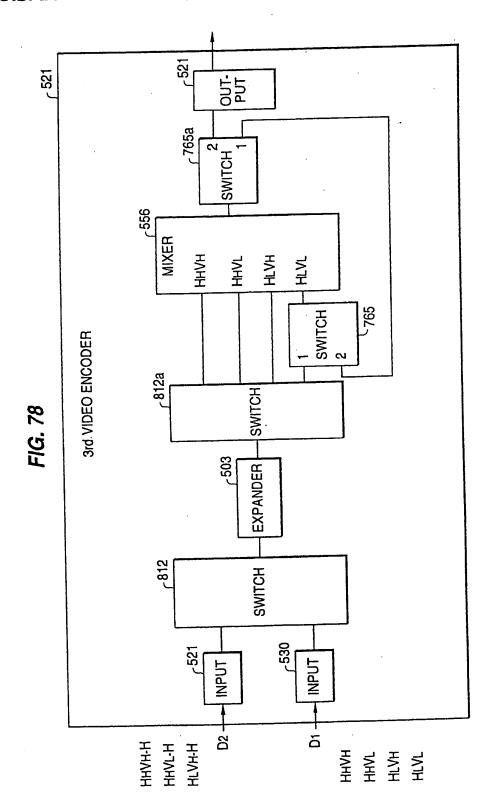
Sheet 72 of 178

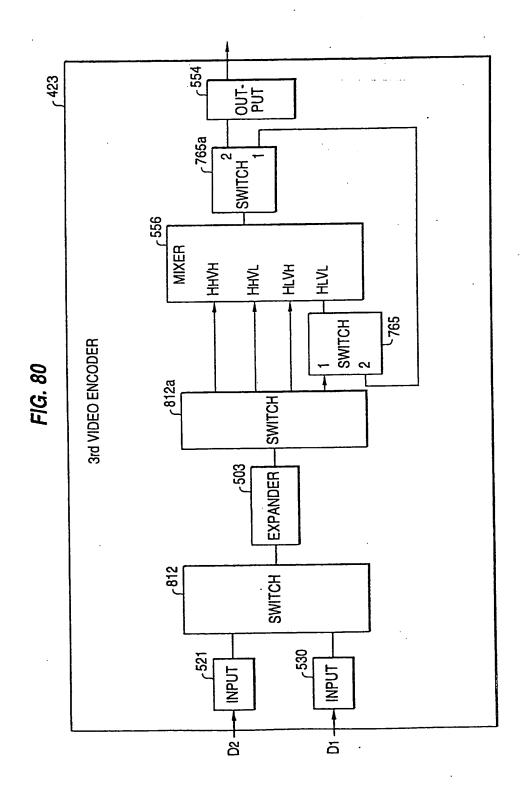
#3 #2 H-HVH-H Ξ TIMING 2 #10 HHVL-H **⊕** <u></u> H\_VH-H 4 9 क 4 HLVH HHVL HHVH TIMING 1 ಬ 段 F HLVL 2 5 20

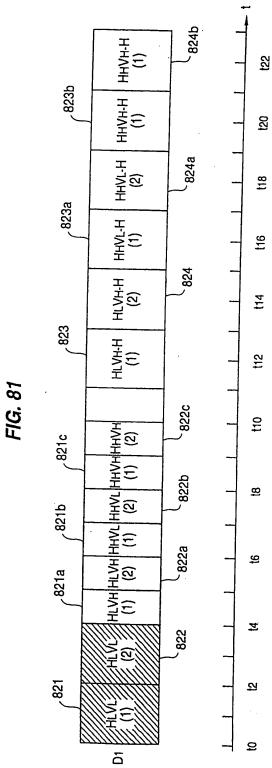


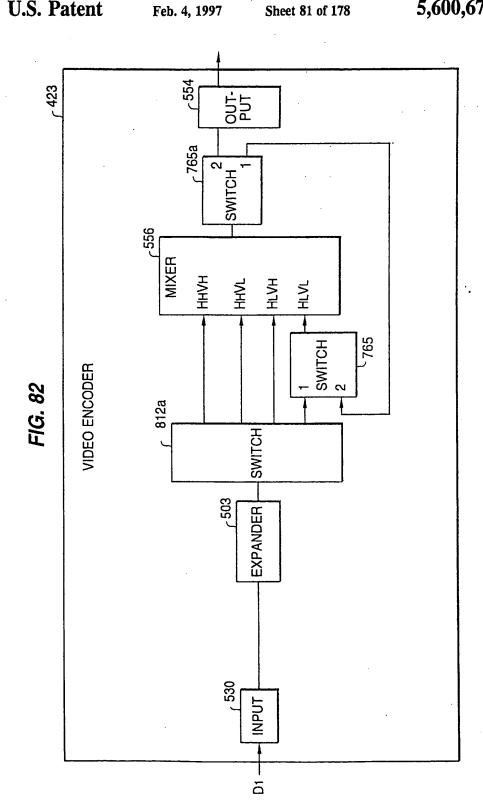




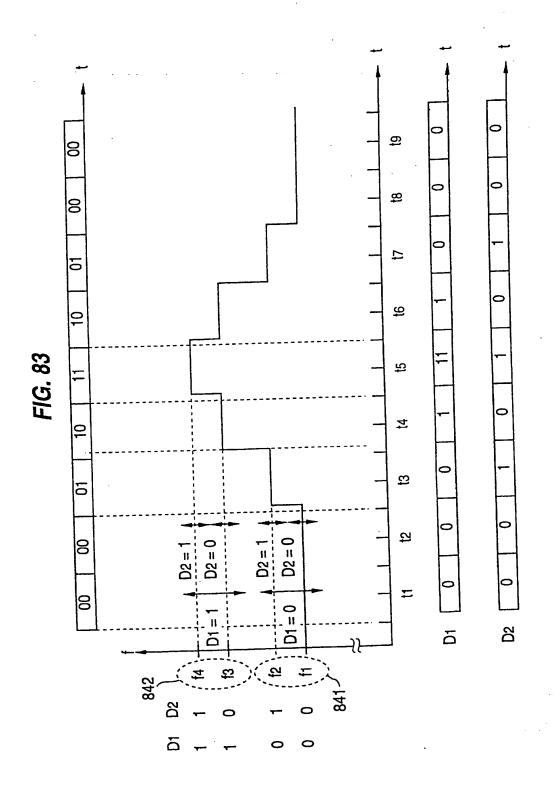


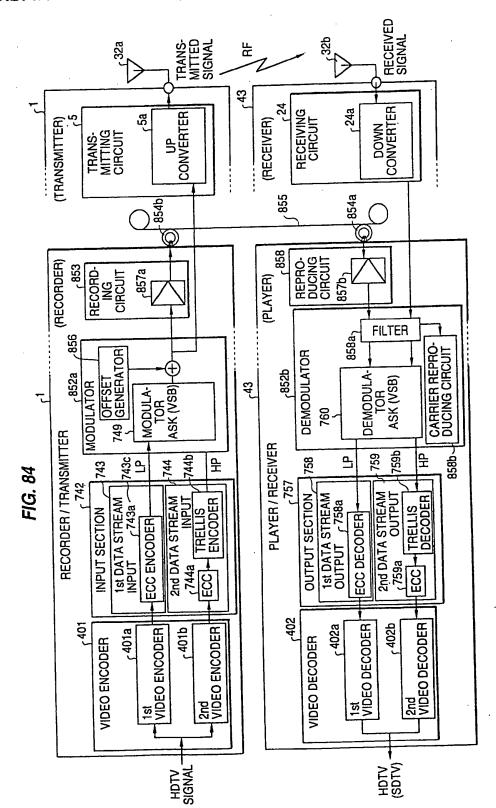


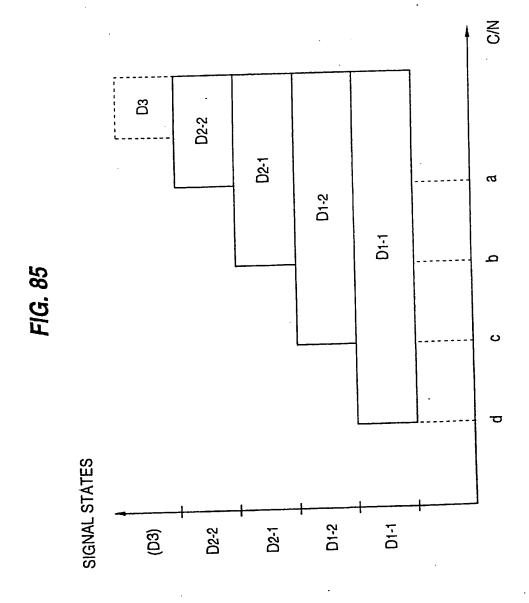


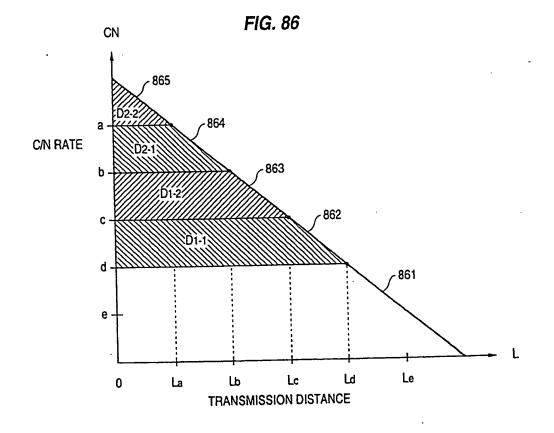


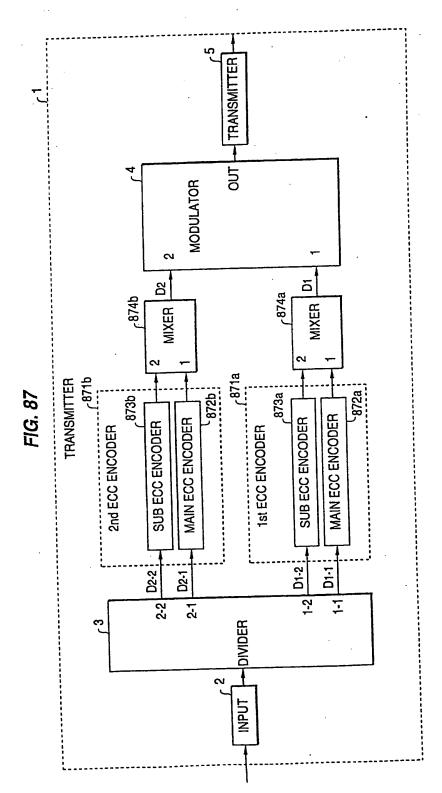
U.S. Patent





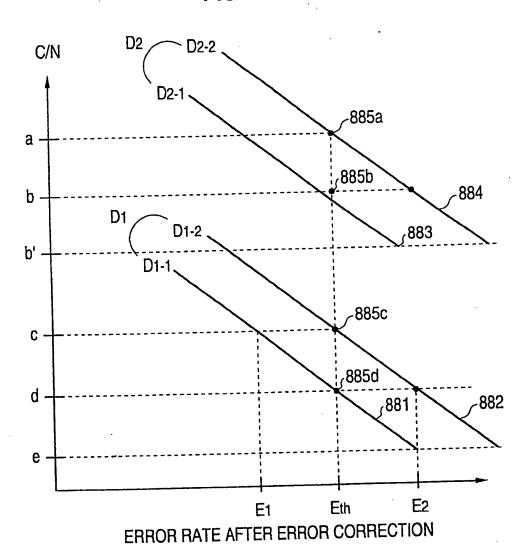


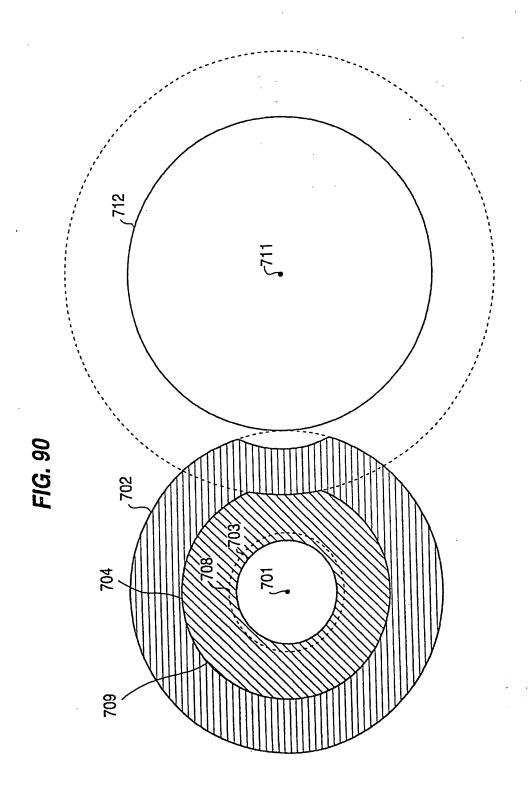




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FIG. 89





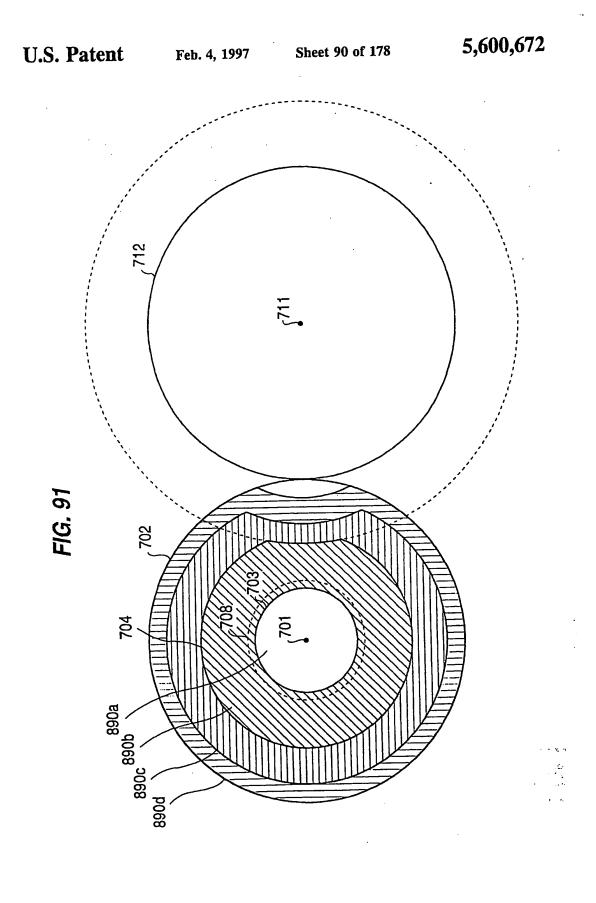
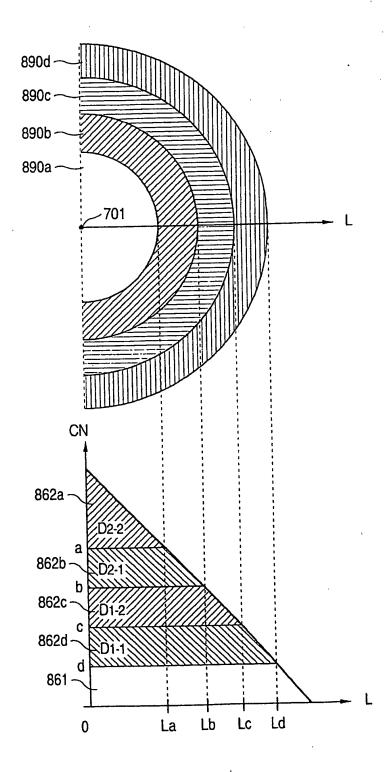
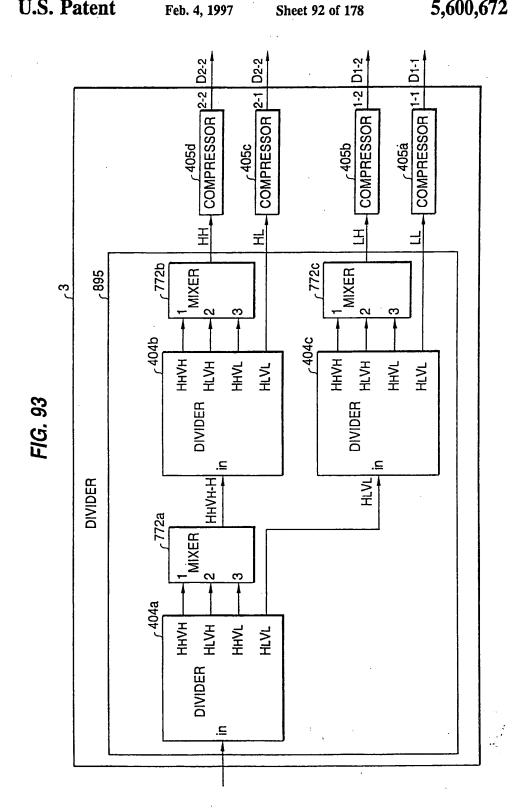
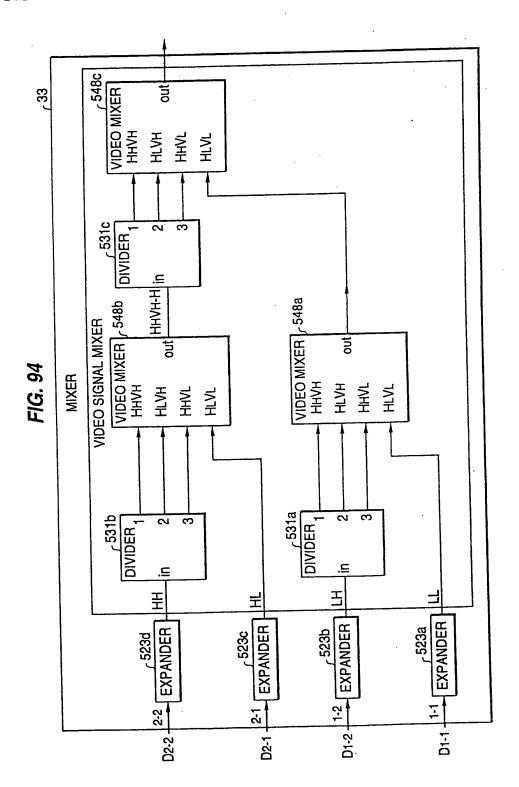


FIG. 92

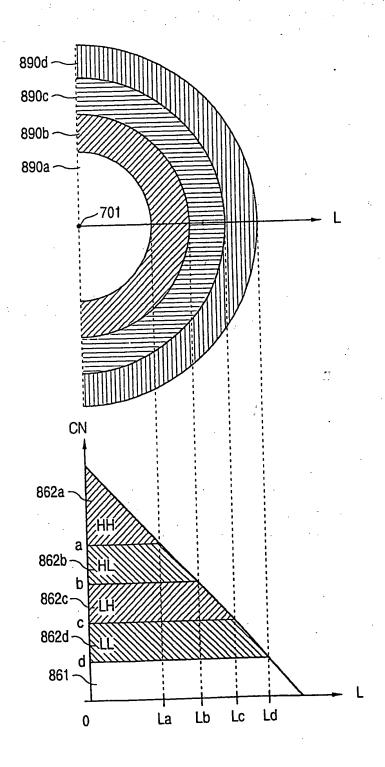


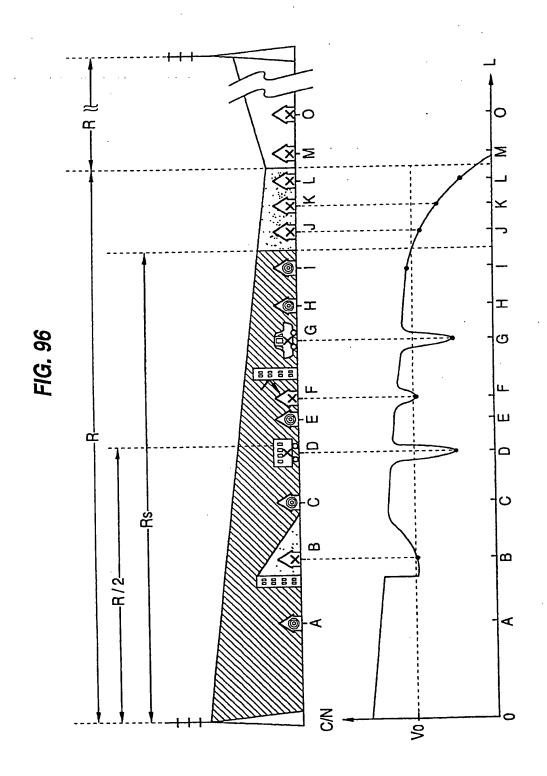




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FIG. 95





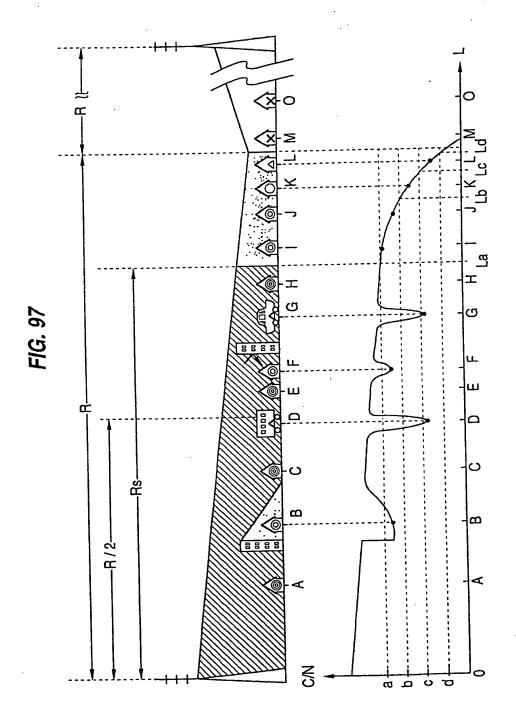


FIG. 98

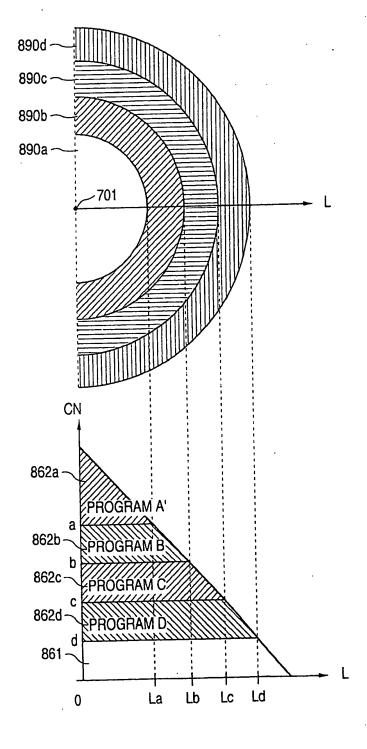


FIG. 100

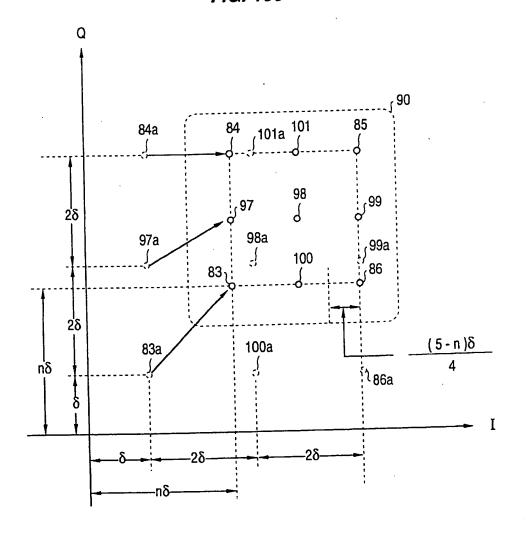


FIG. 101

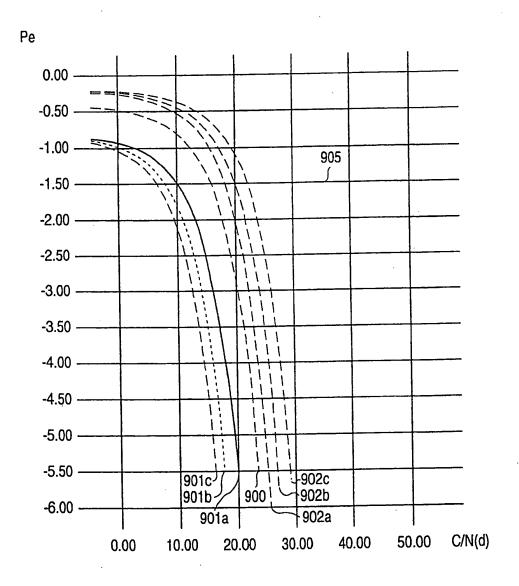


FIG. 102

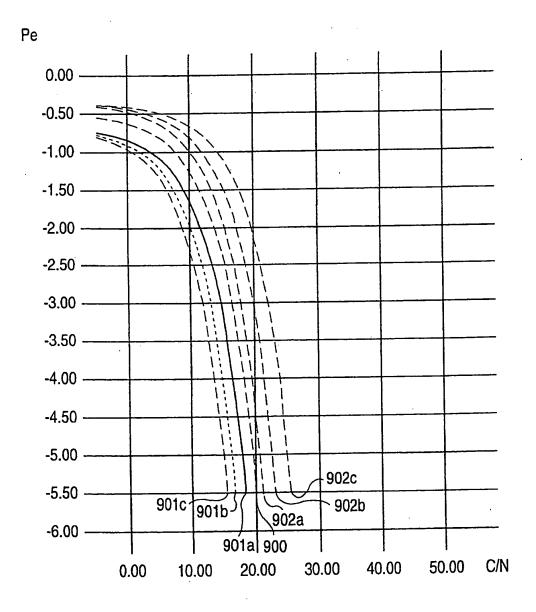


FIG. 103

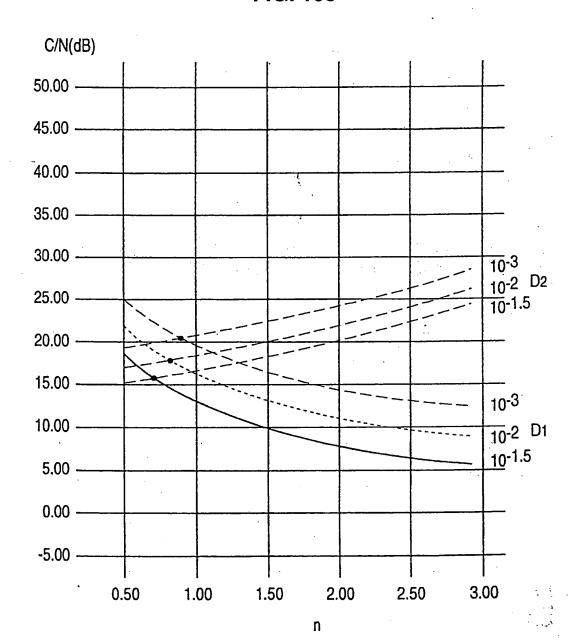
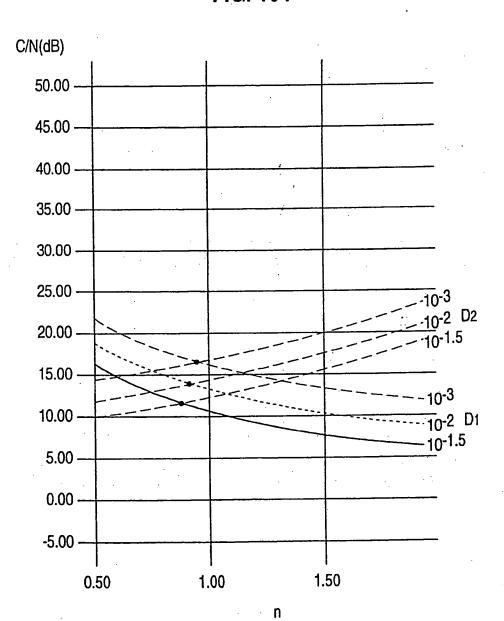
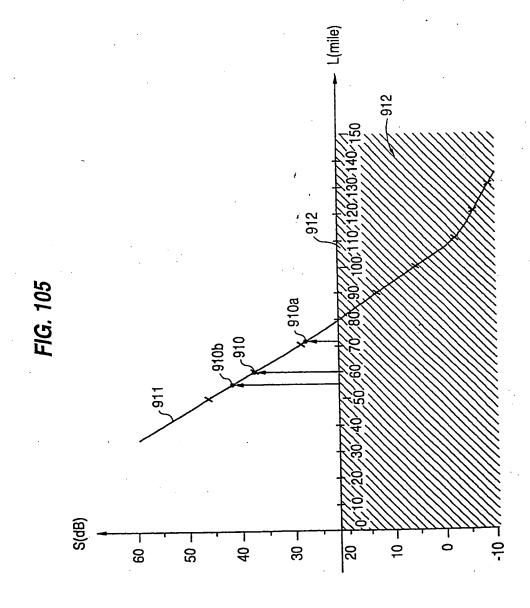
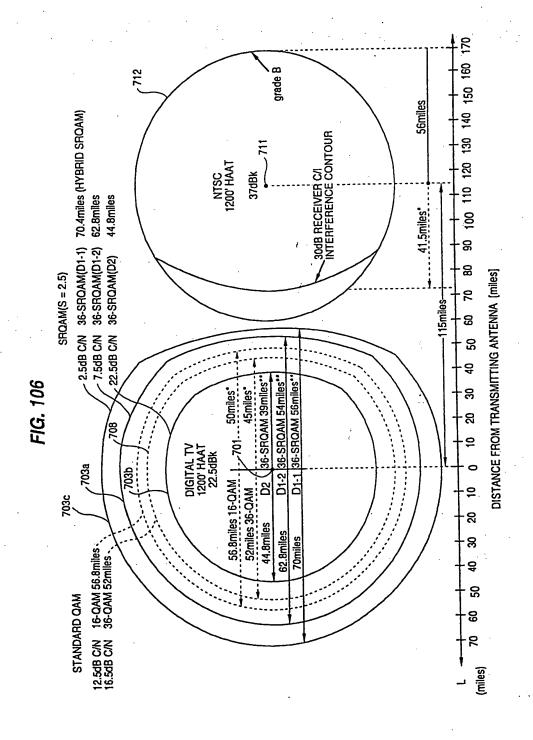
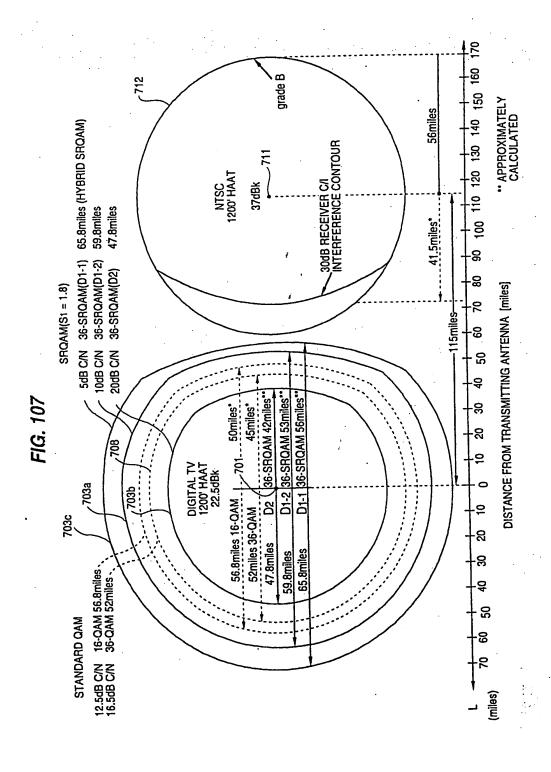


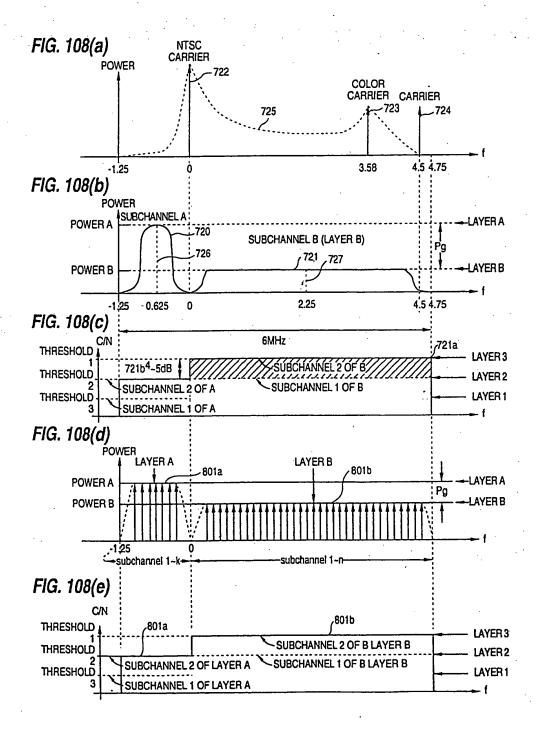
FIG. 104

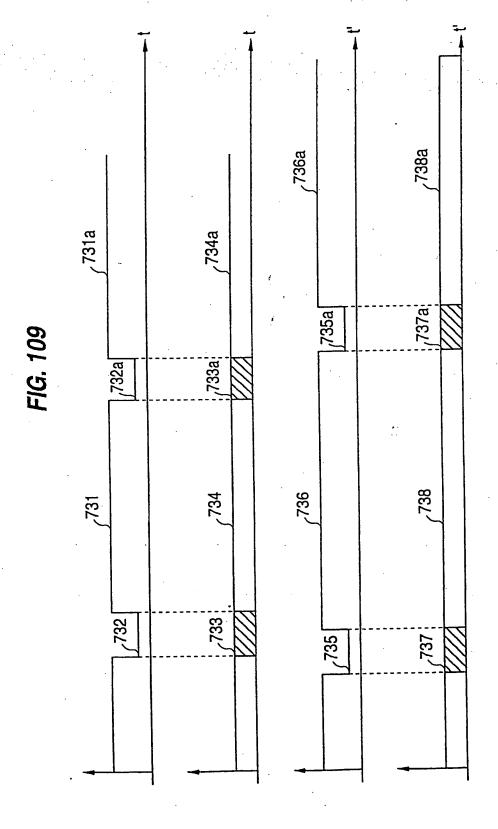












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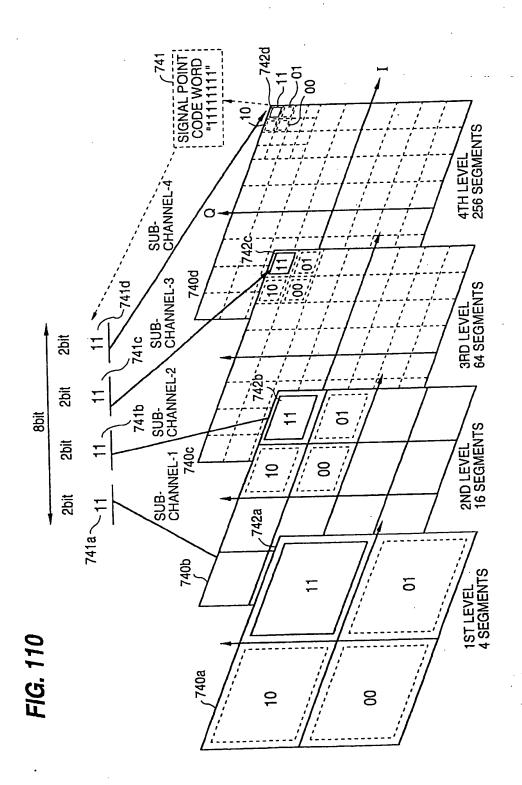


FIG. 111

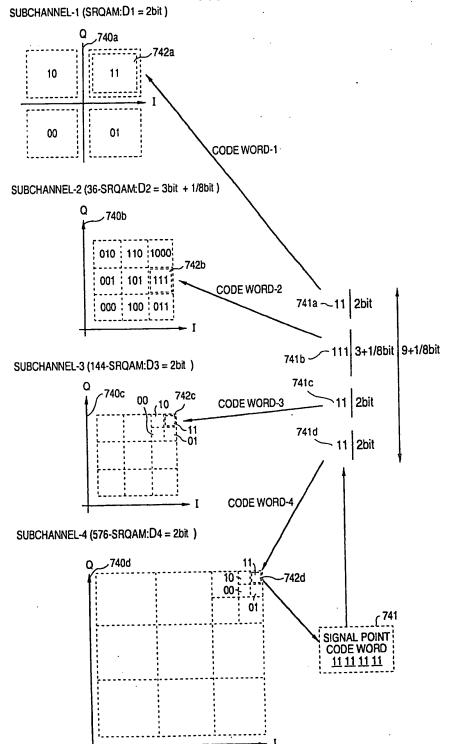
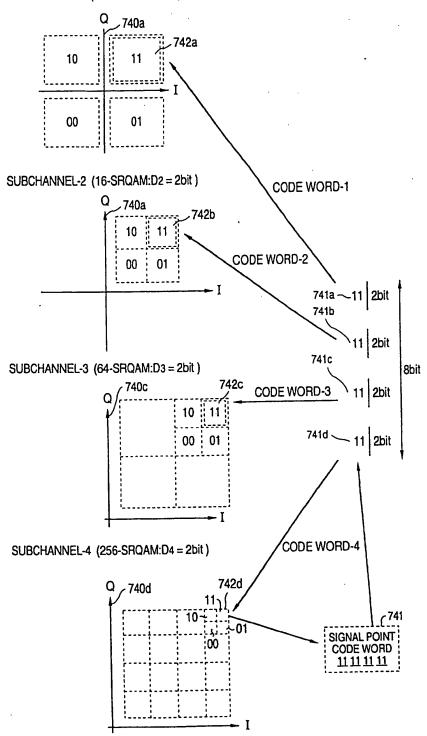
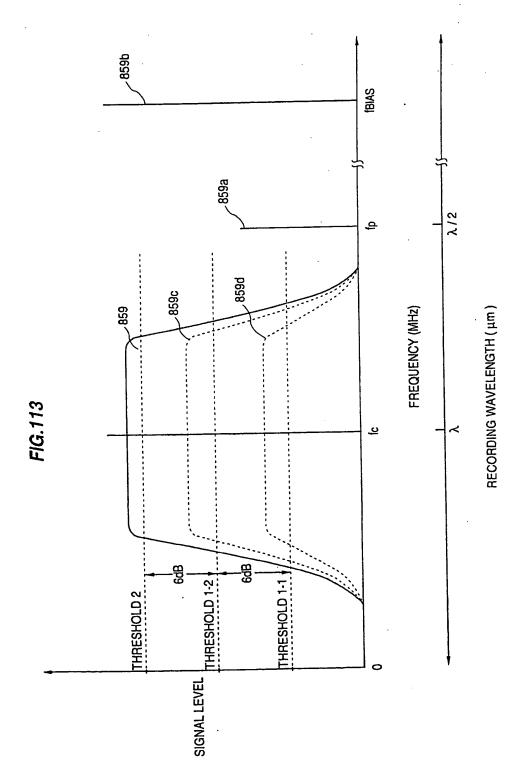
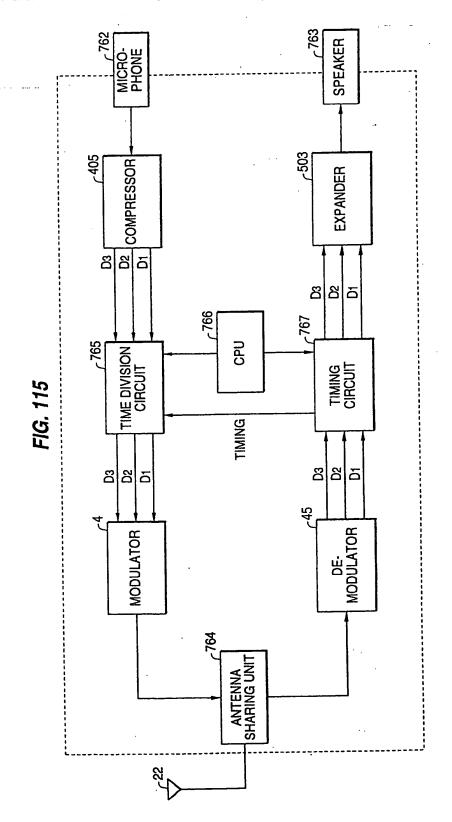


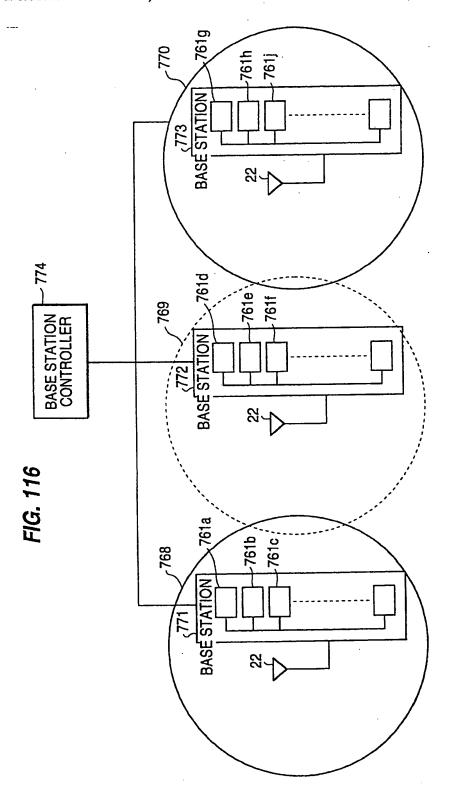
FIG. 112

SUBCHANNEL-1 (SRQAM:D1 = 2bit)

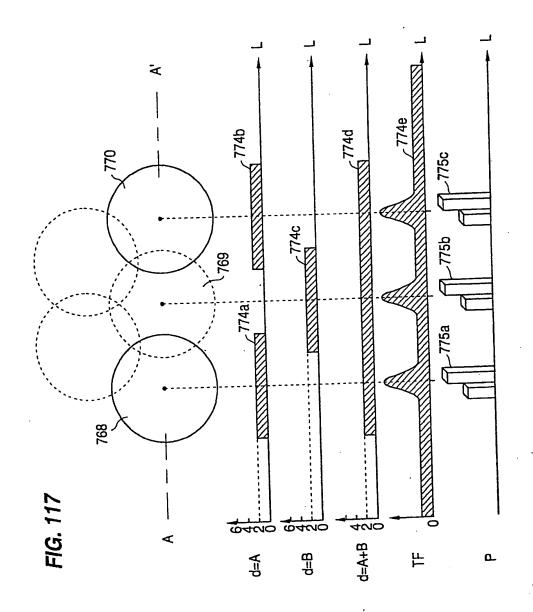


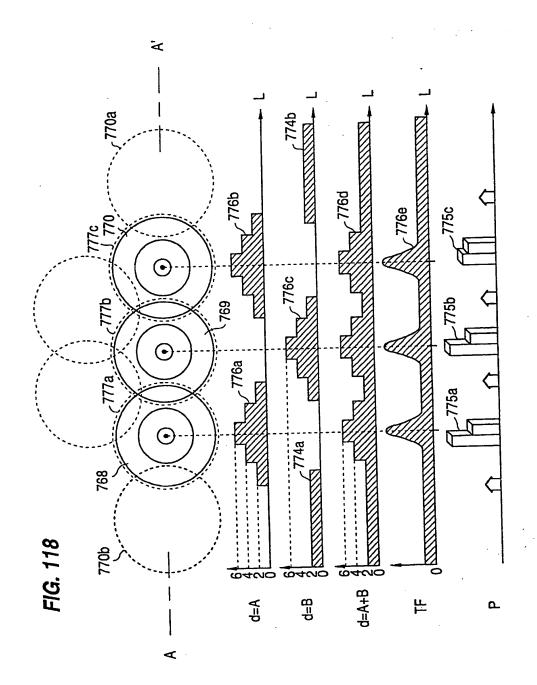


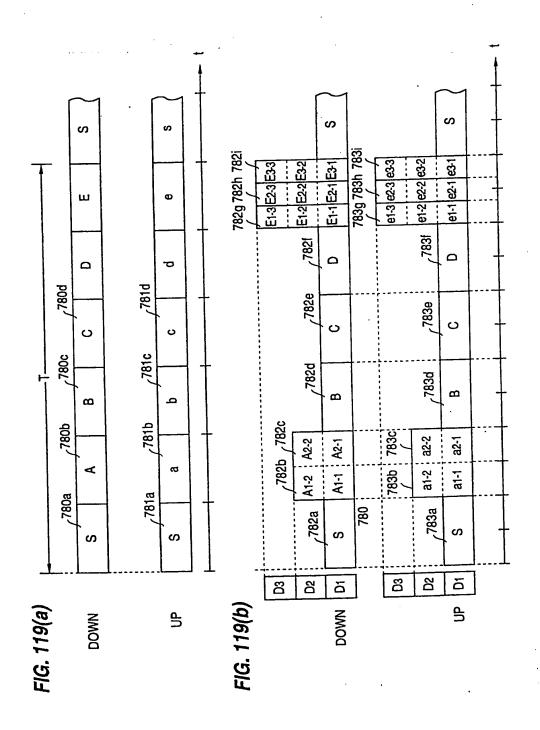


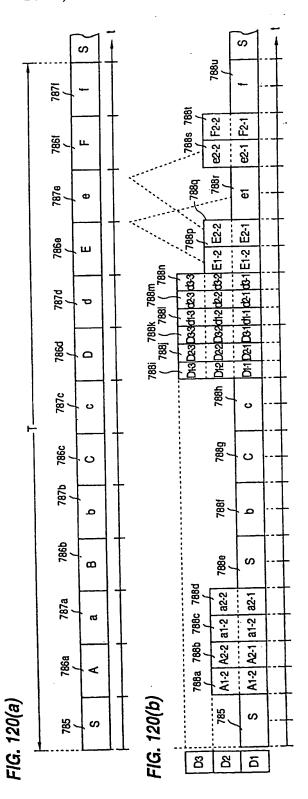


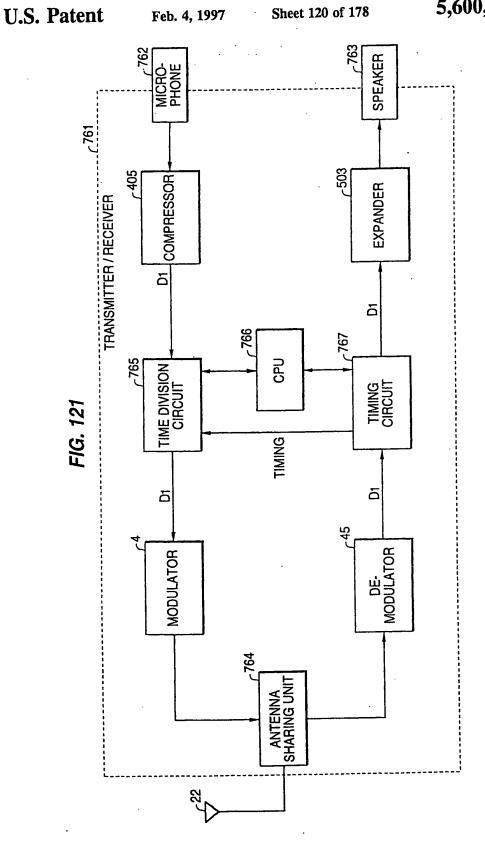
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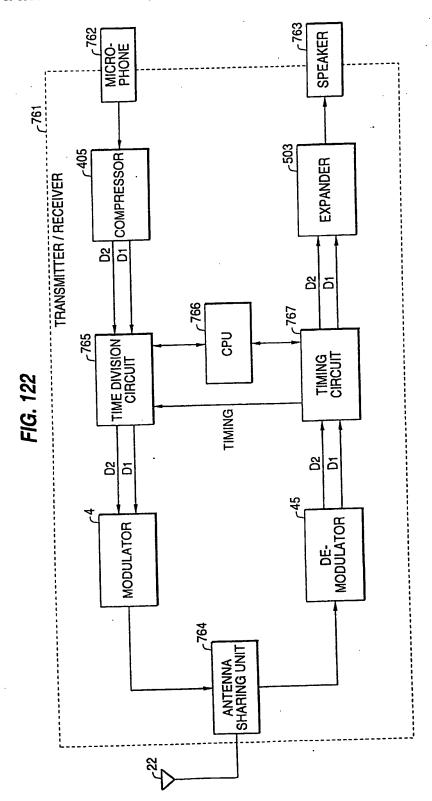


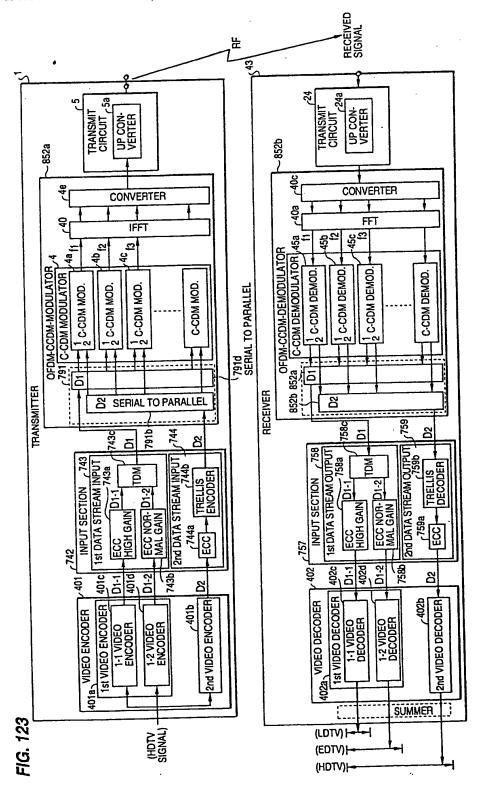




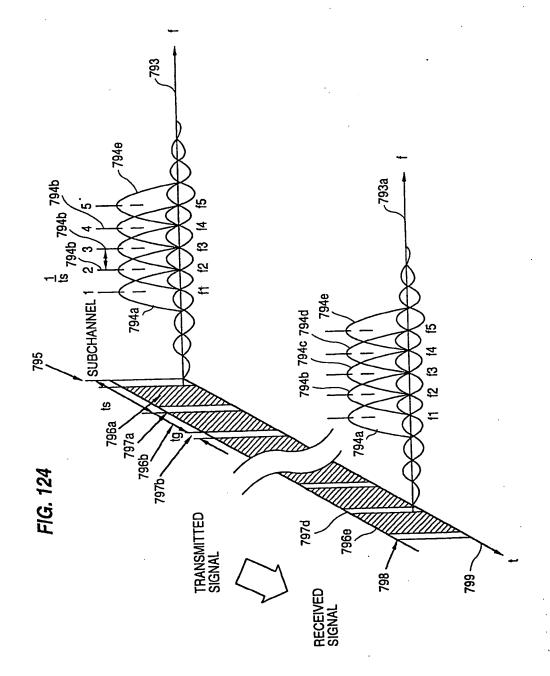


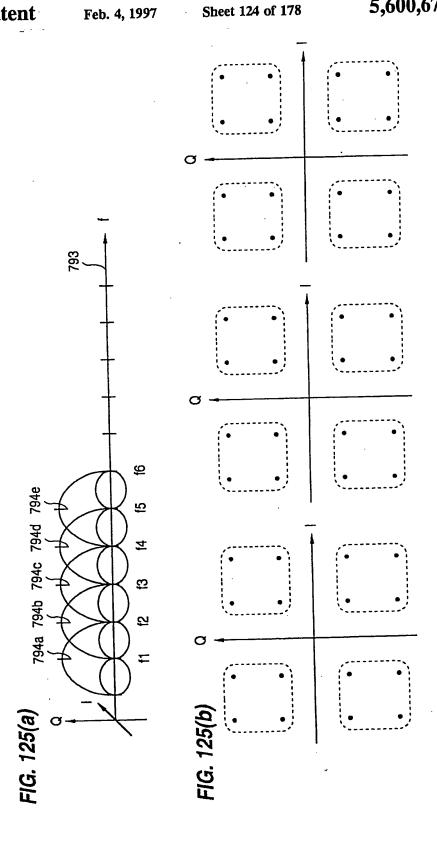


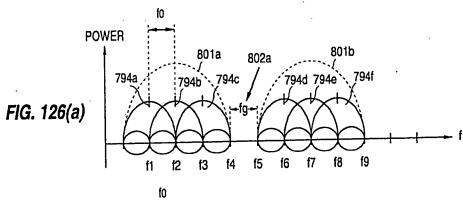


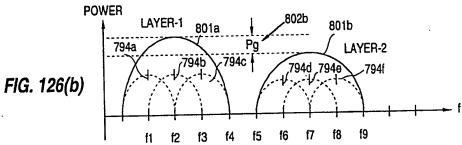


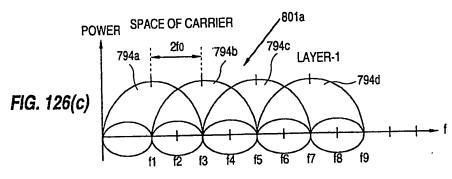
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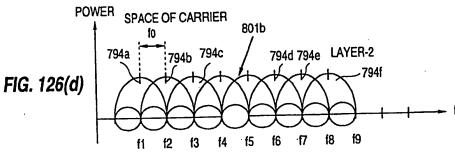












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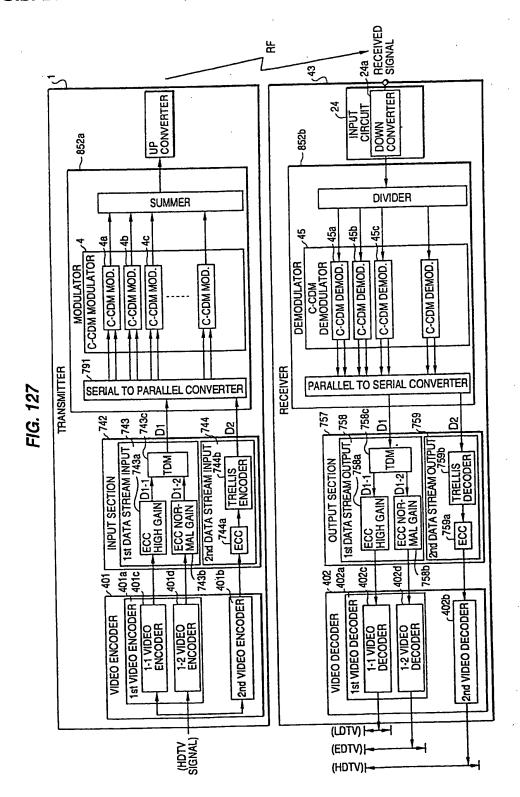


FIG. 128(a)

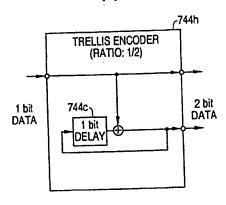


FIG. 128(d)

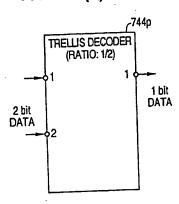


FIG. 128(b)

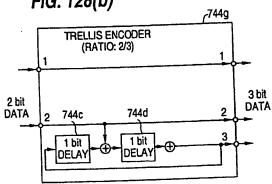


FIG. 128(e)

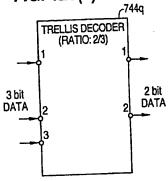


FIG. 128(c)

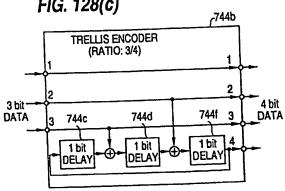
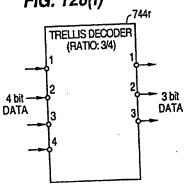
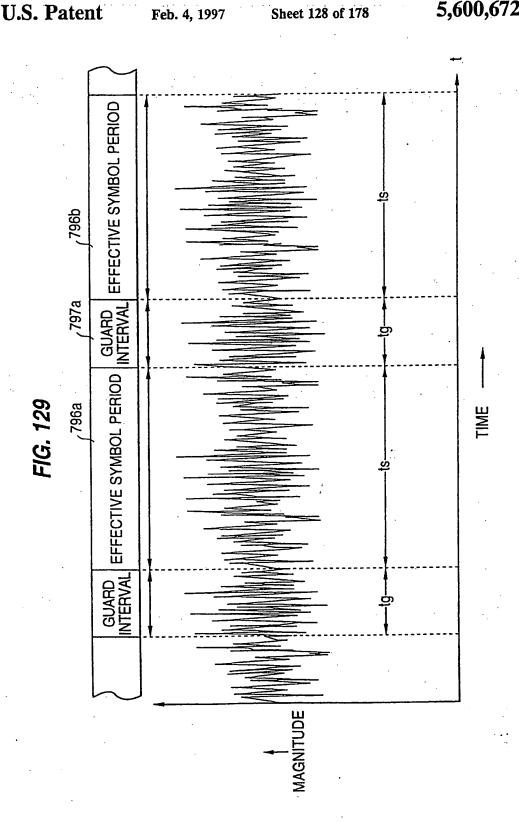
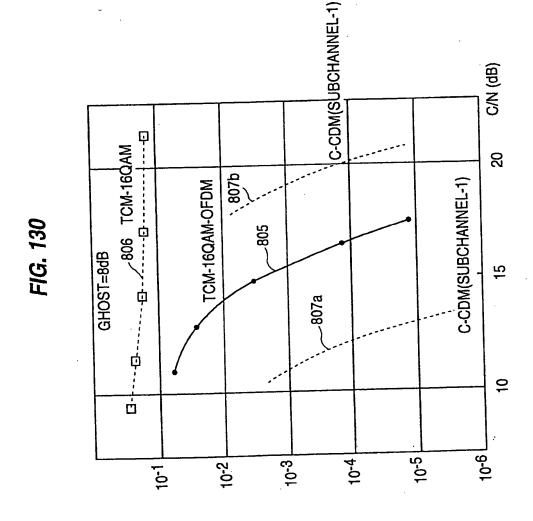


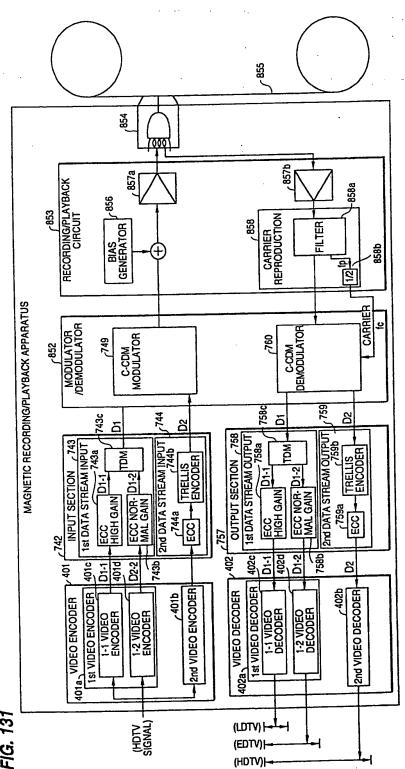
FIG. 128(f)

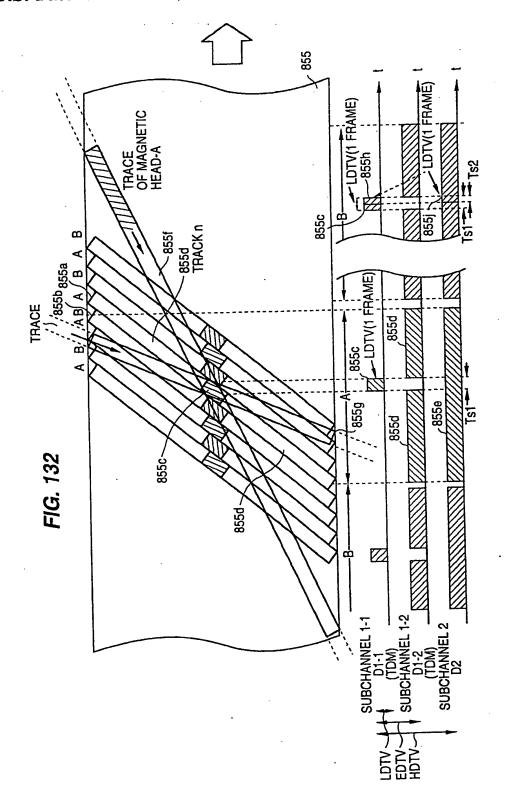


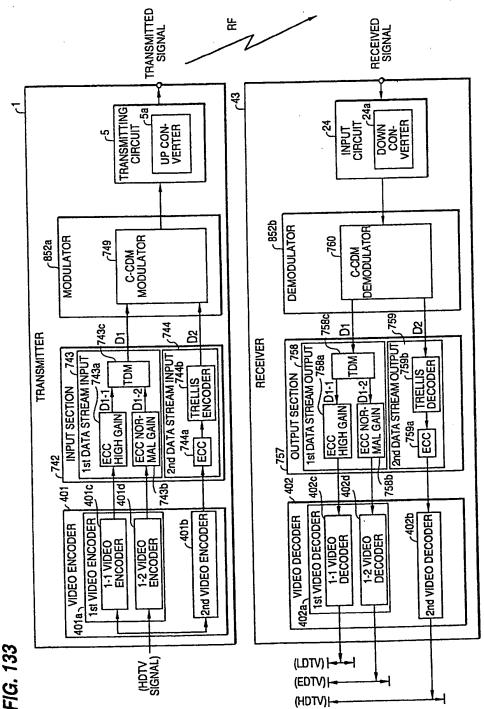


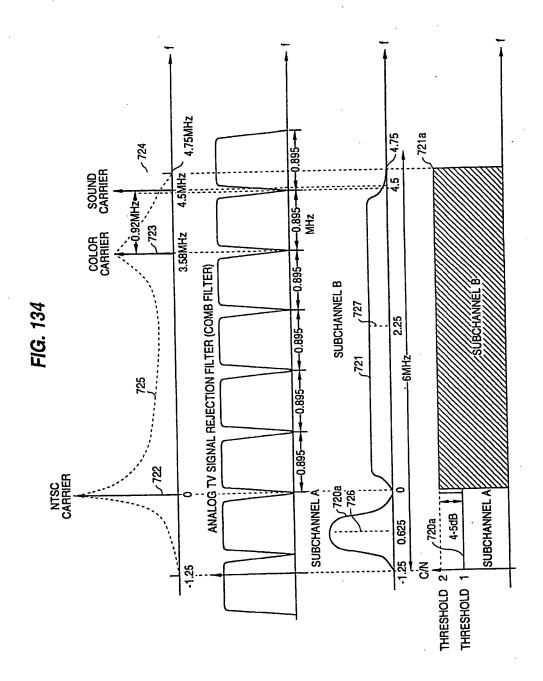
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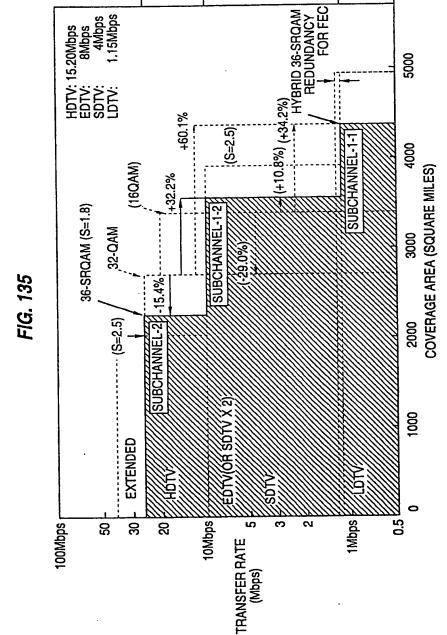




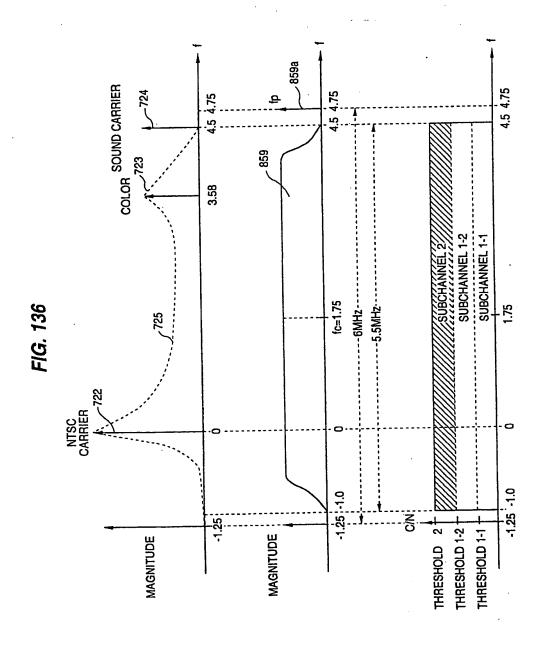


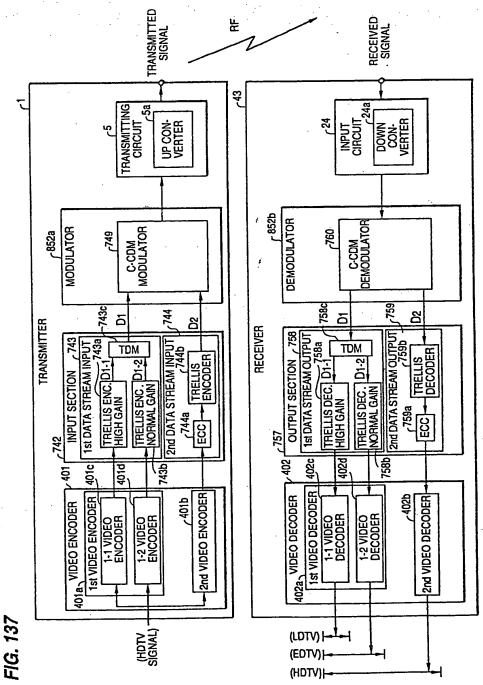


RECEIVER-3
RECEIVER-2
RECEIVER-1



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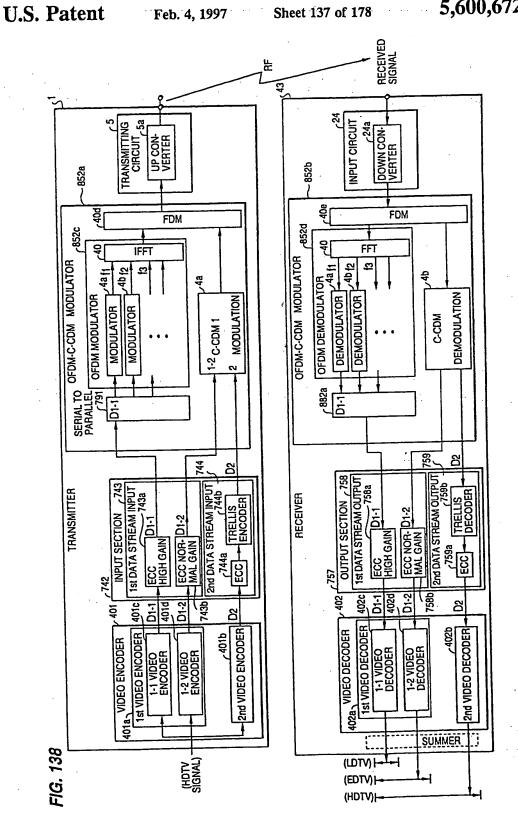


FIG. 139

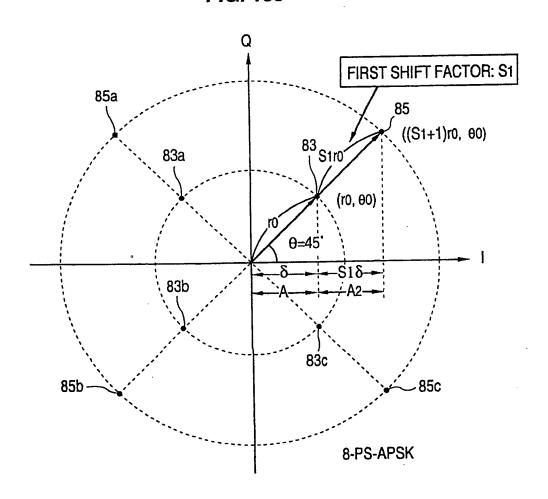


FIG. 140

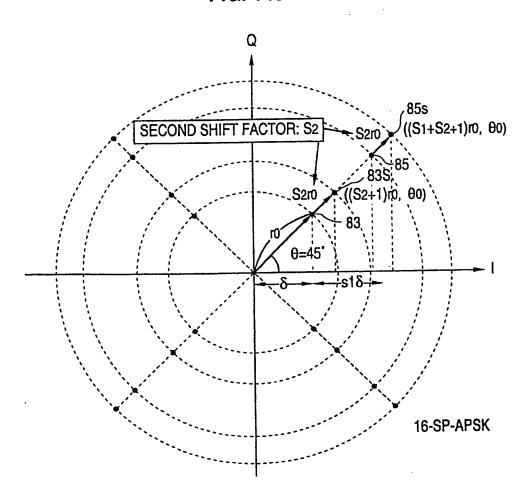


FIG. 141

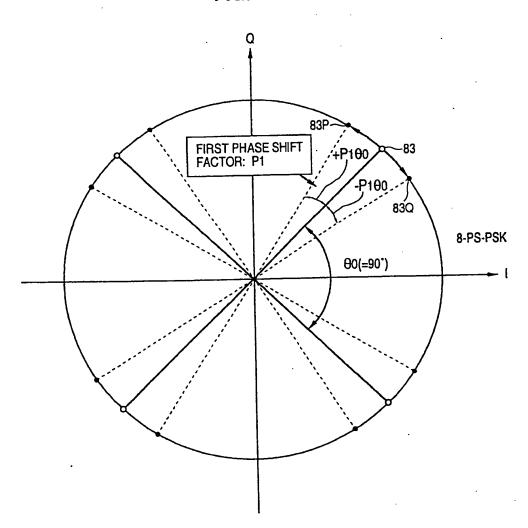
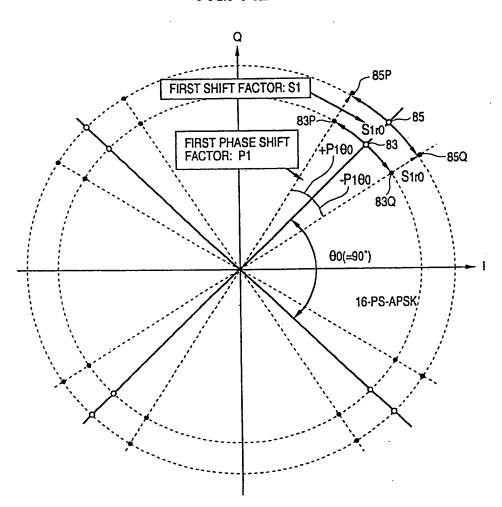
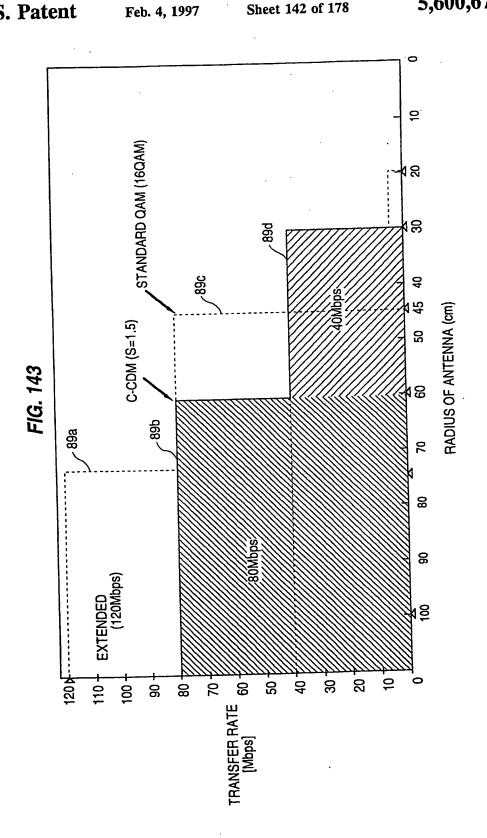


FIG. 142





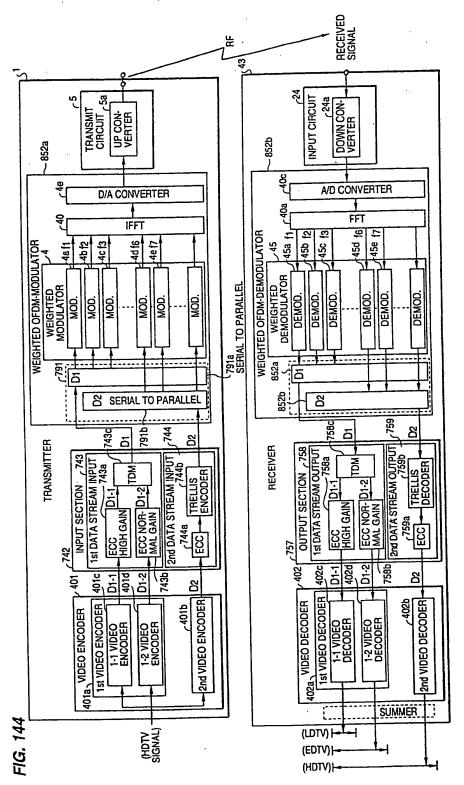


FIG. 145(a)

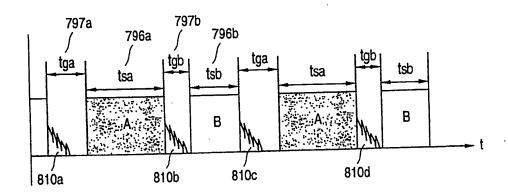
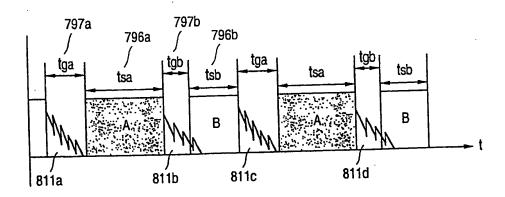
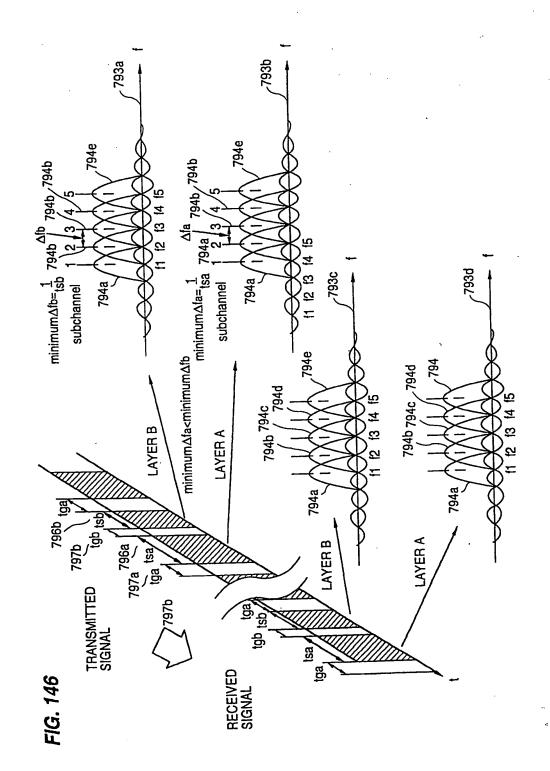
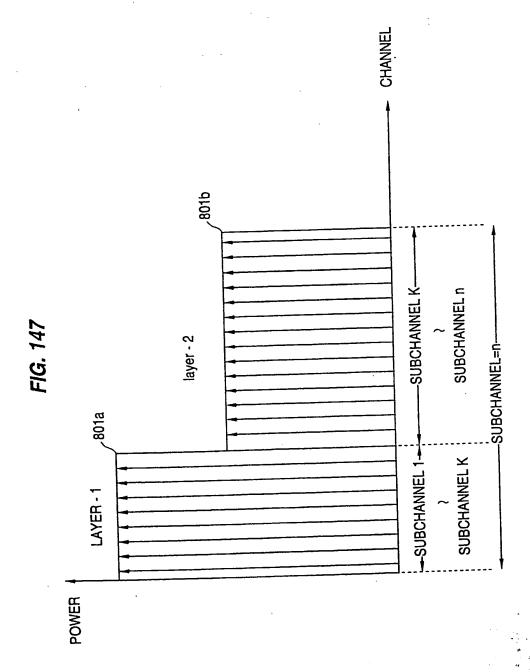
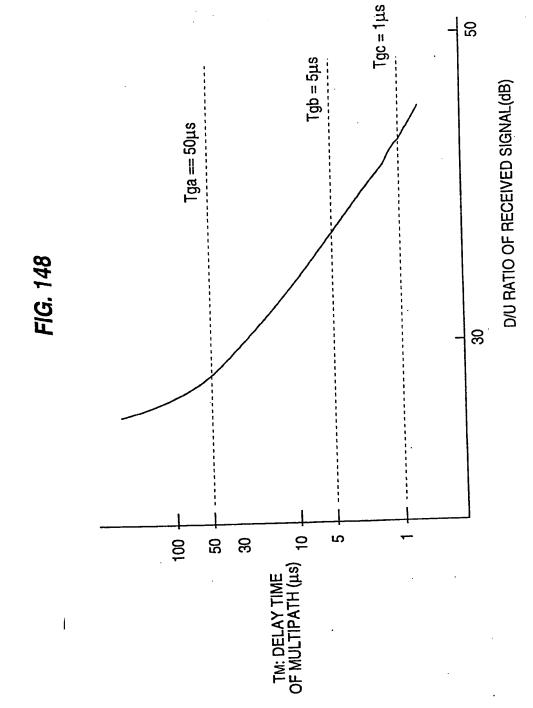


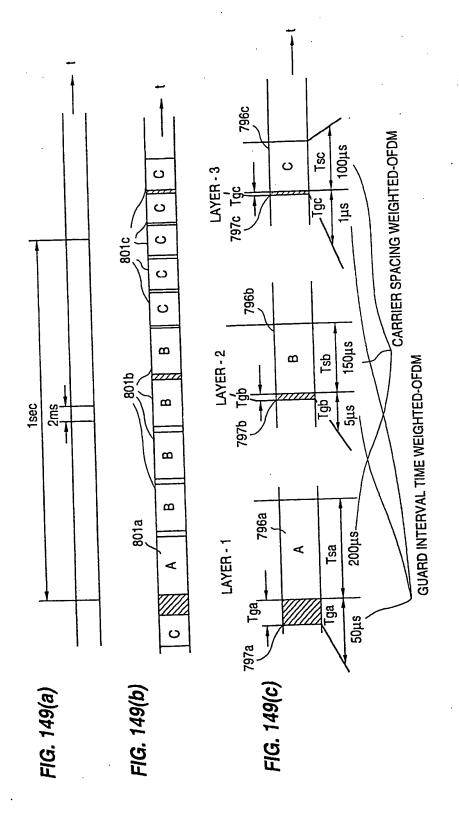
FIG. 145(b)

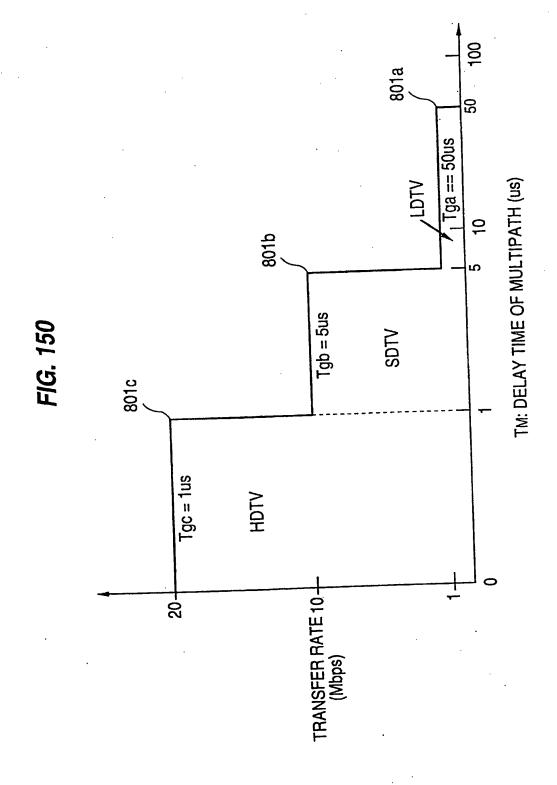




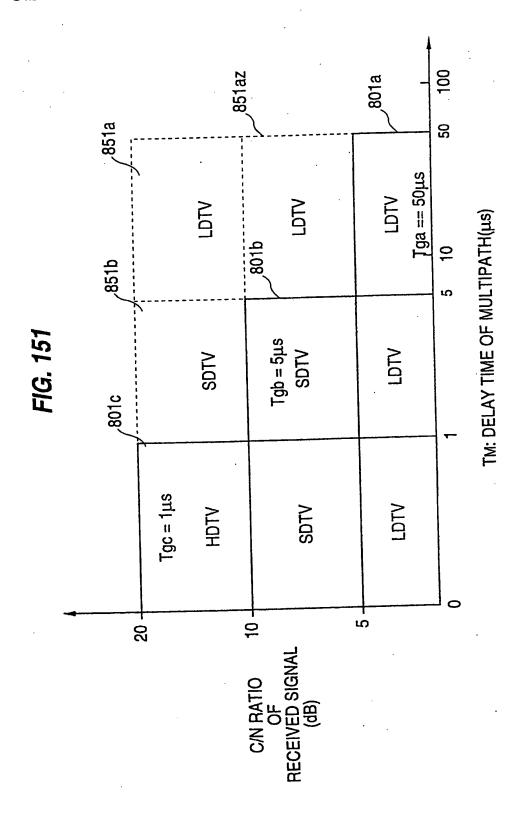


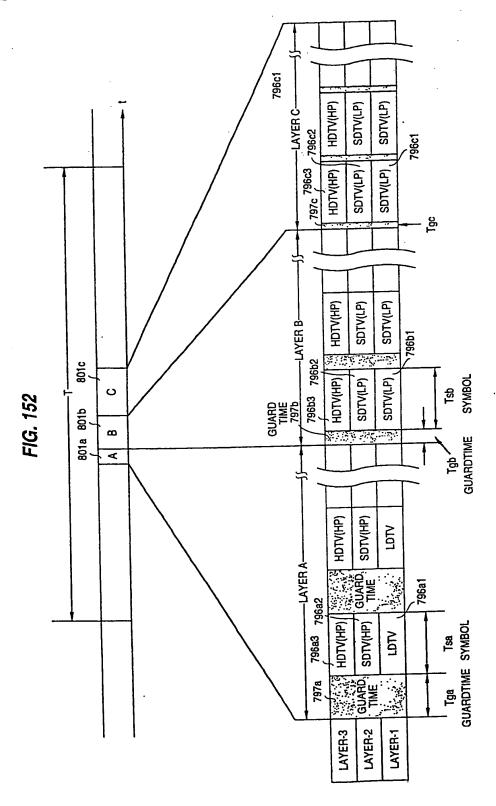




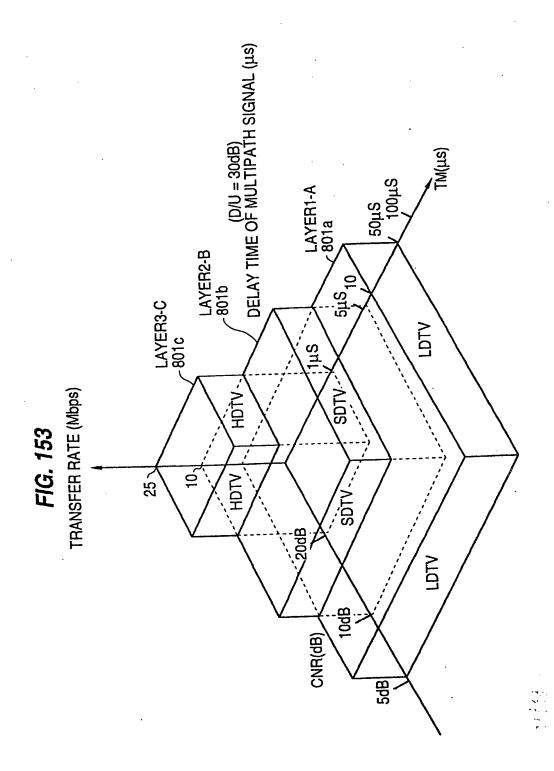


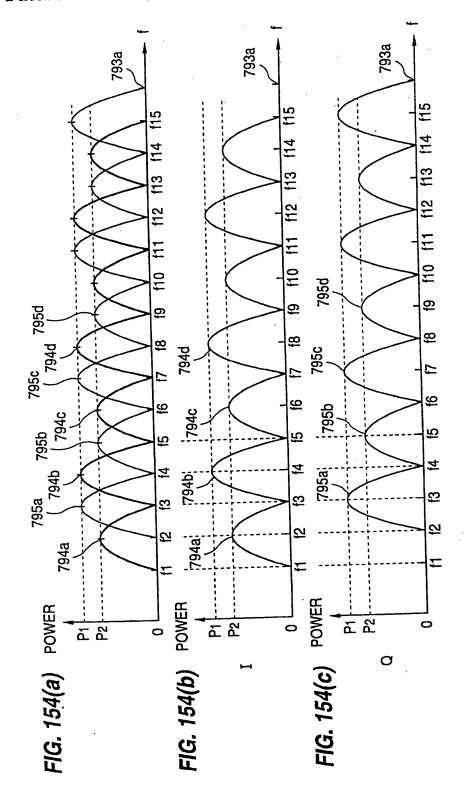


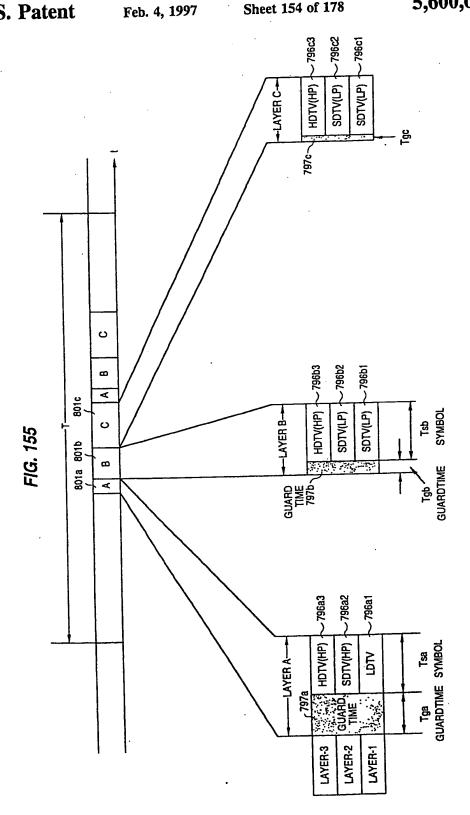


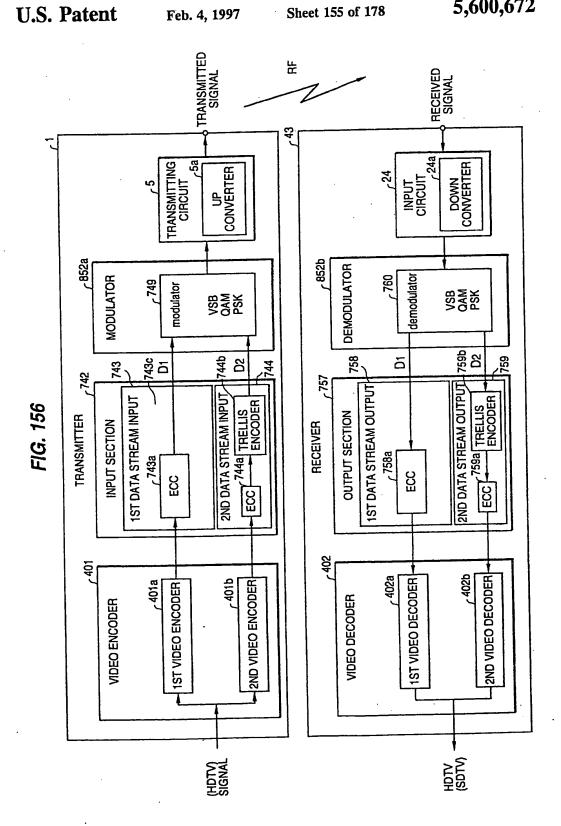


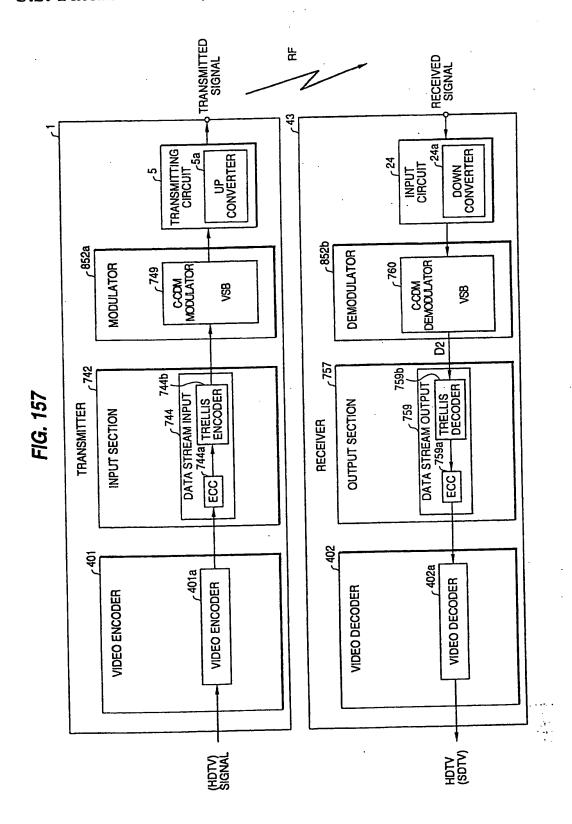


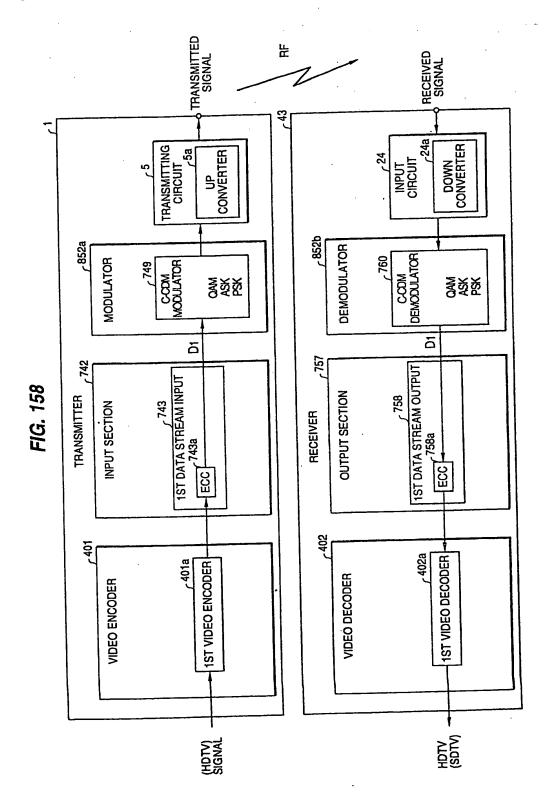






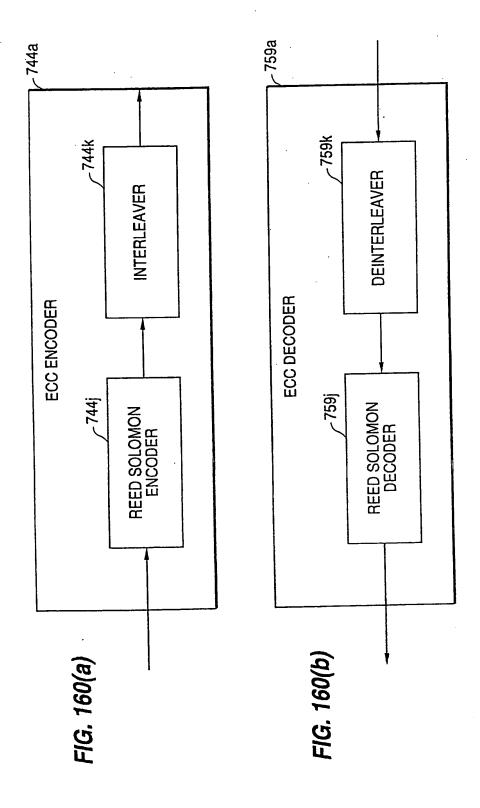


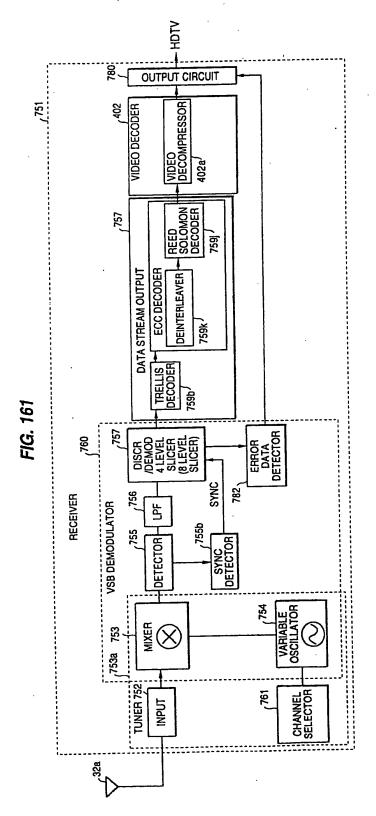


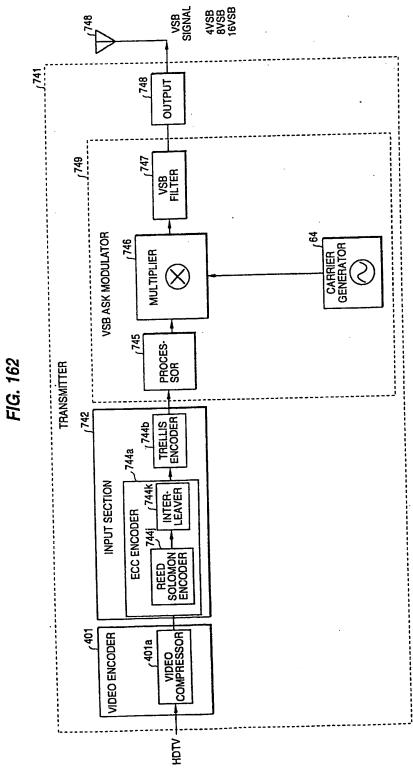


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FIG. 163

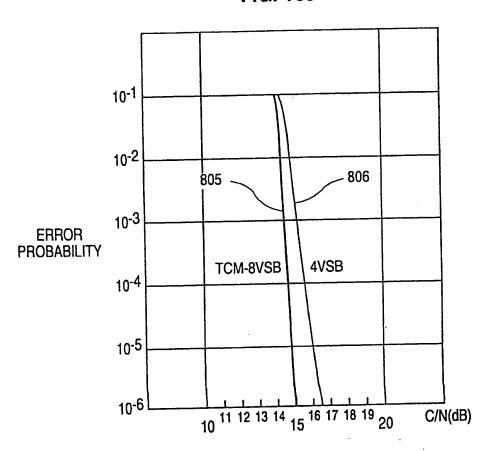
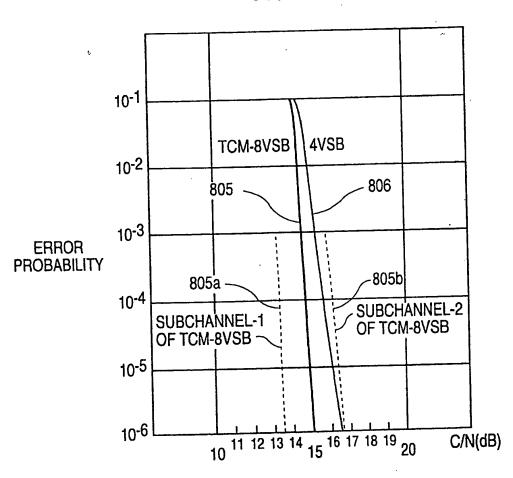


FIG. 164



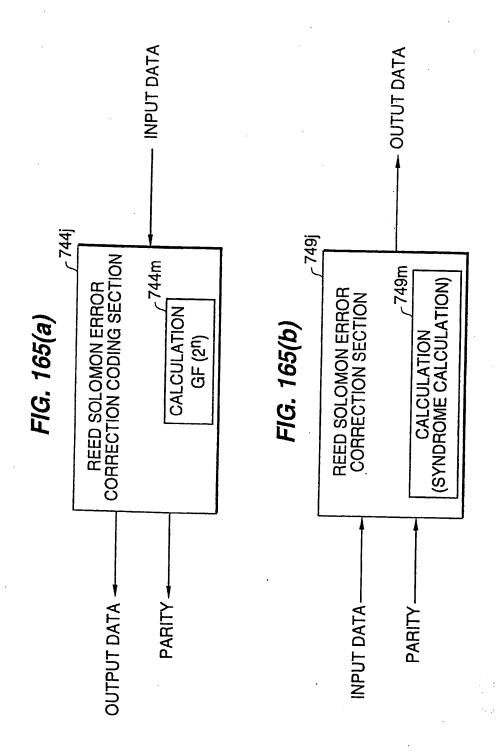
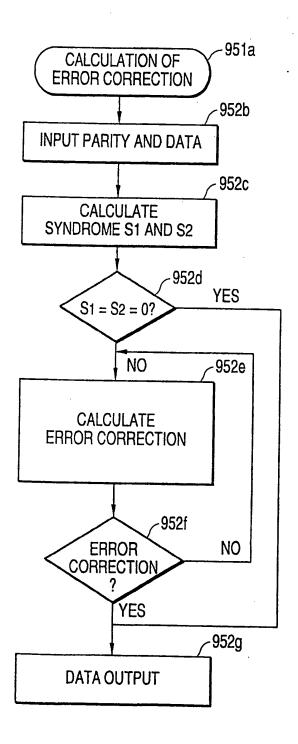
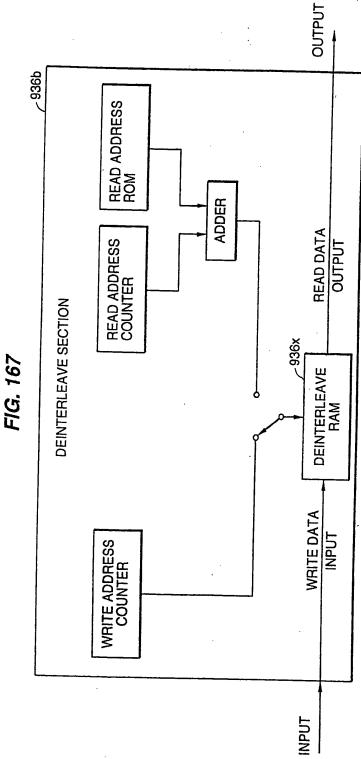


FIG. 166





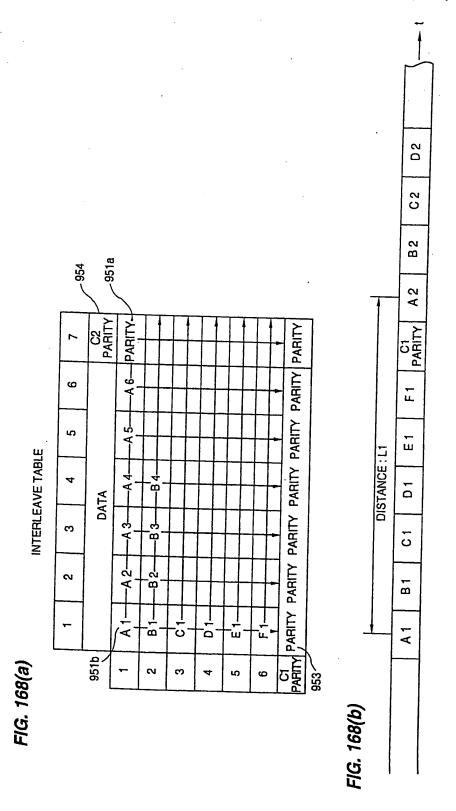
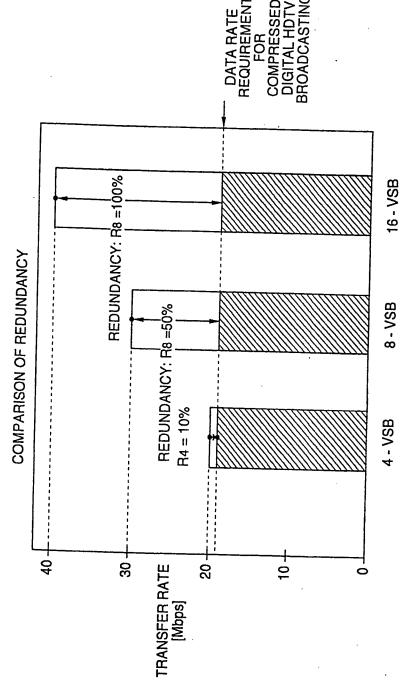


FIG. 169



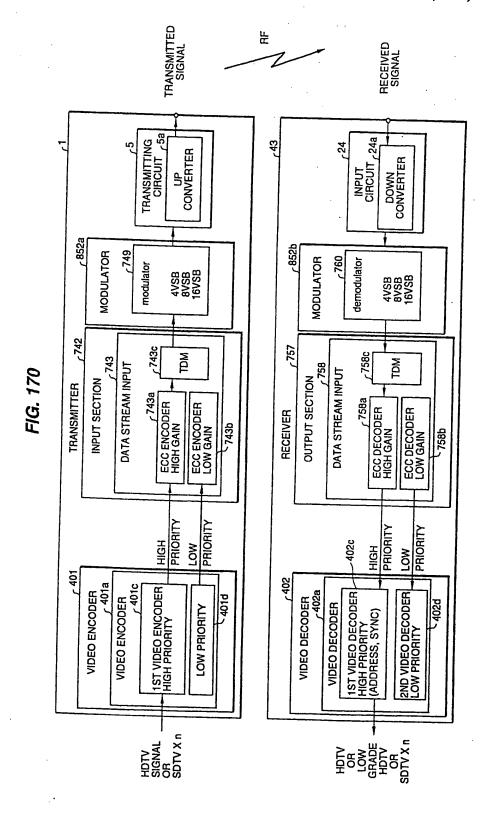
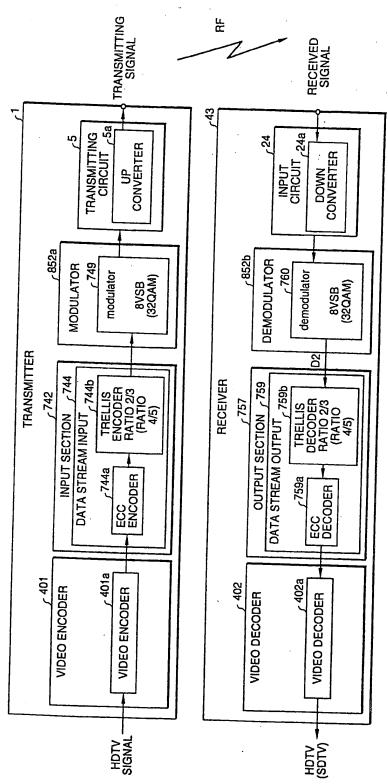


FIG. 171



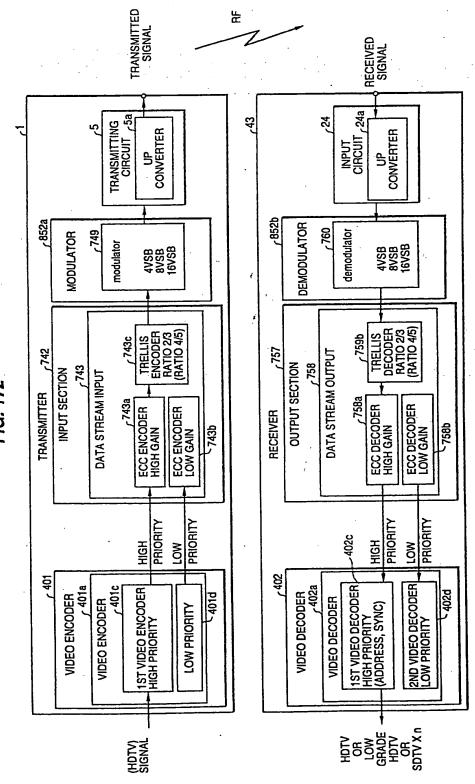
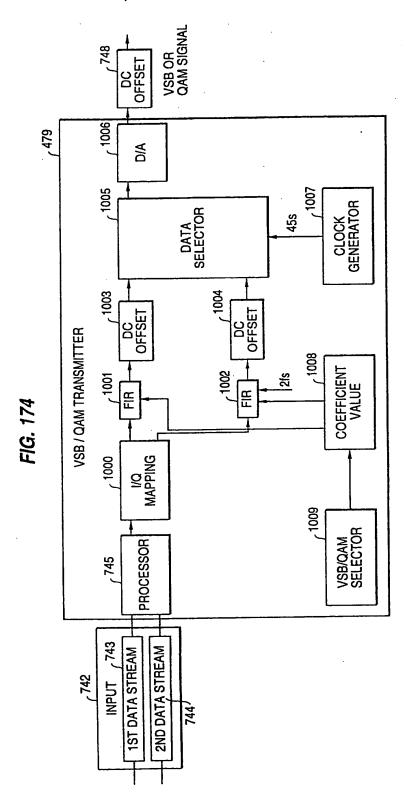
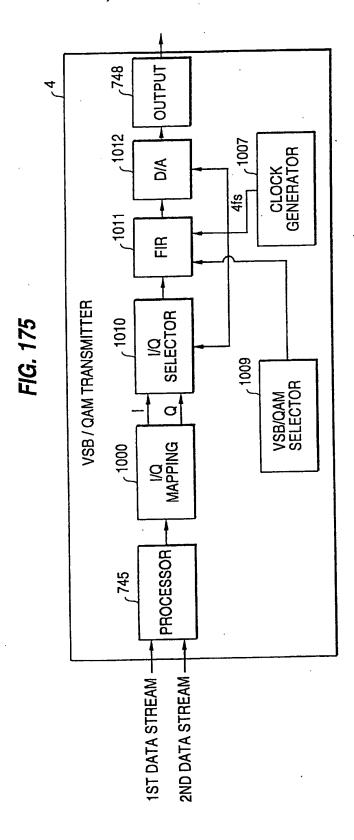
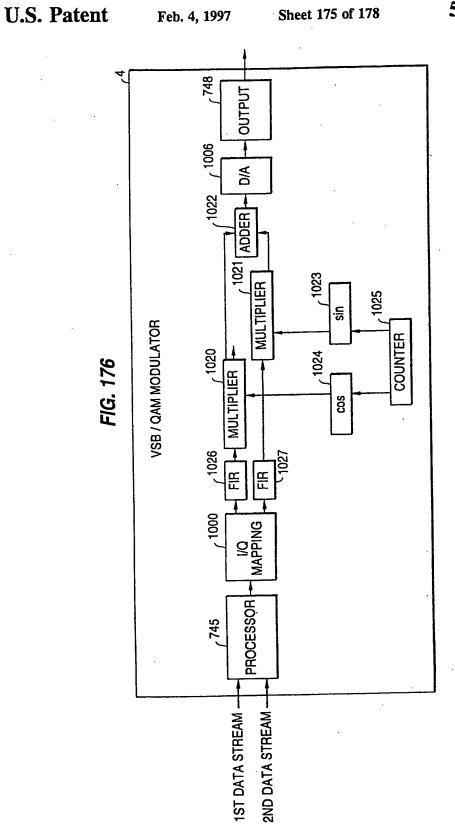
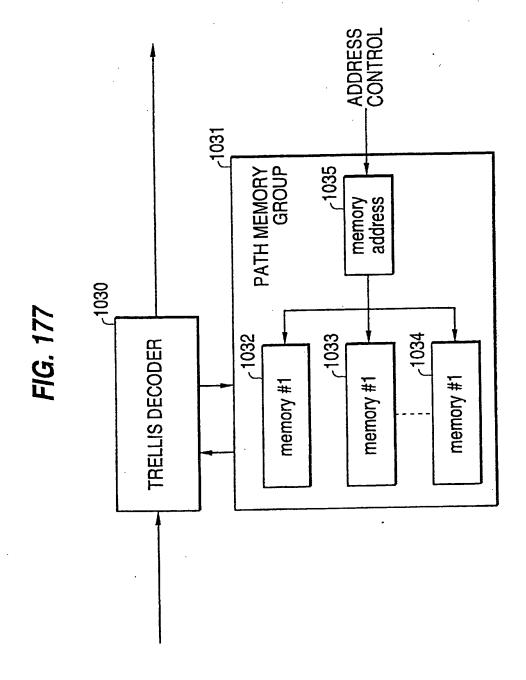


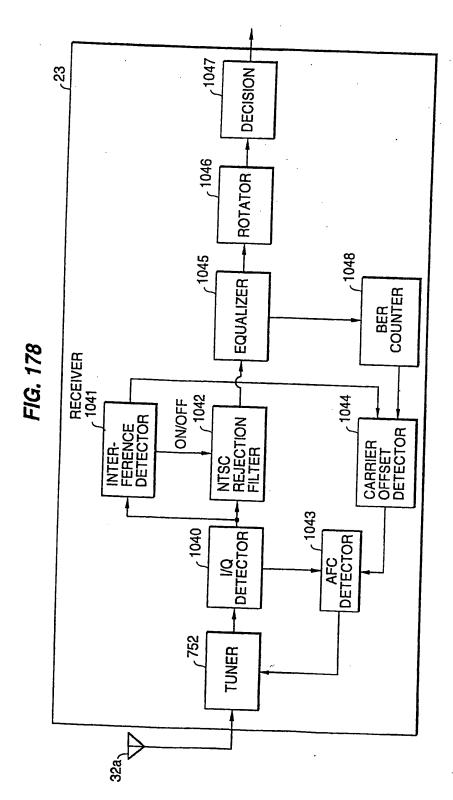
FIG. 172











DECISION AFC DET RS DECODER EQUALIZER CLOCK REPRODUCER AGC 1057 ROLL OFF FILTER FIG. 179 VSB/QAM RECEIVER GATE SIGNAL AFC CLOCK 1069 1053 CONVERTER VSB det 1051 BW/6MHz TUNER